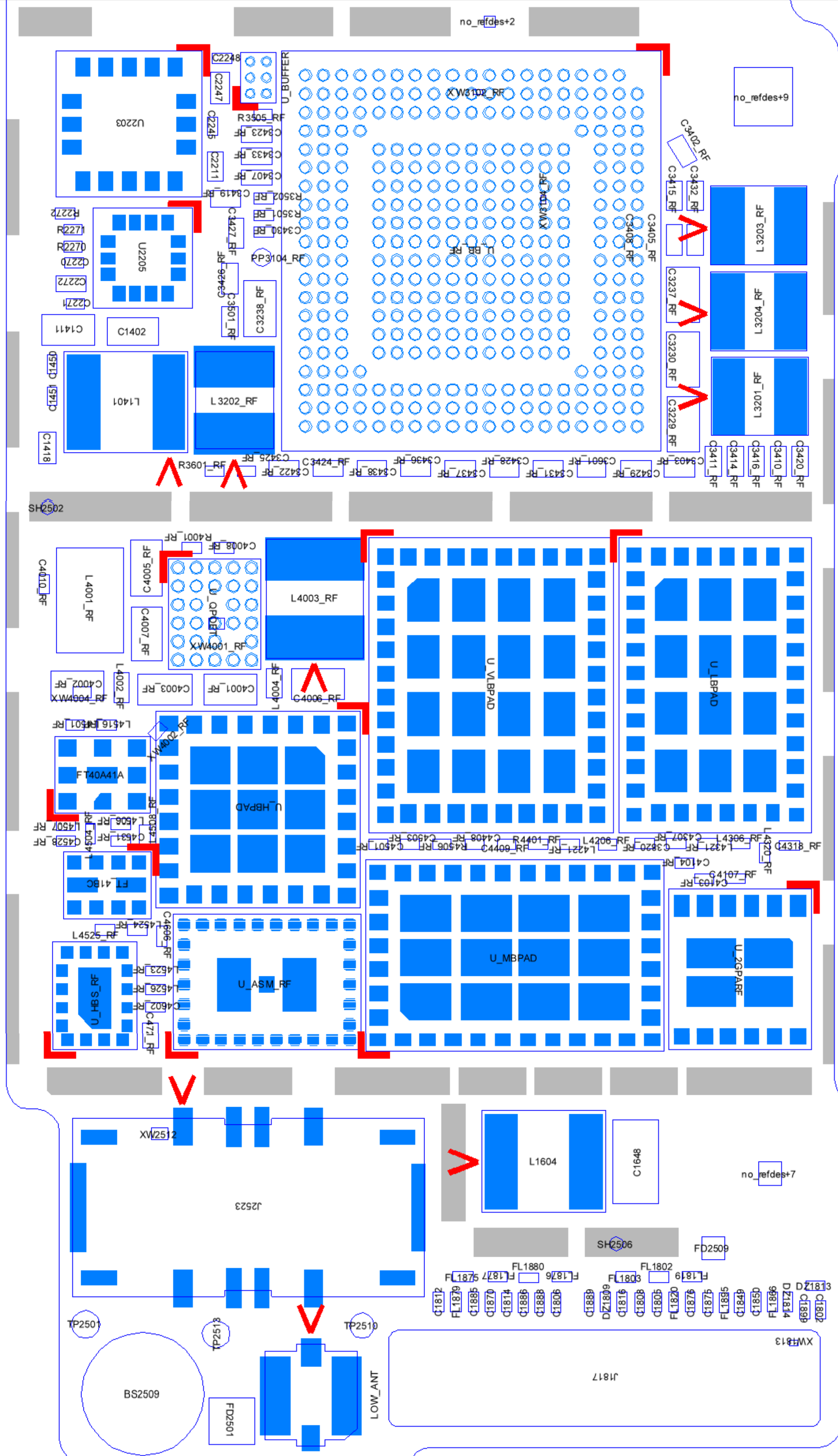
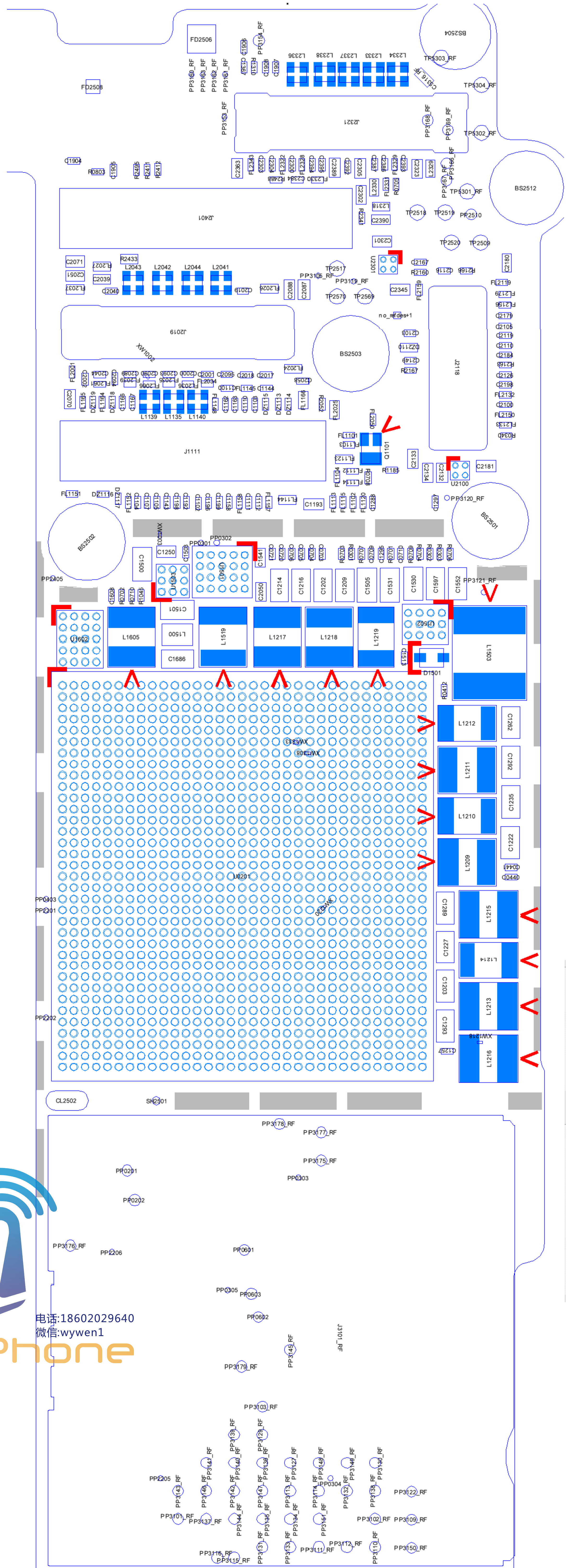
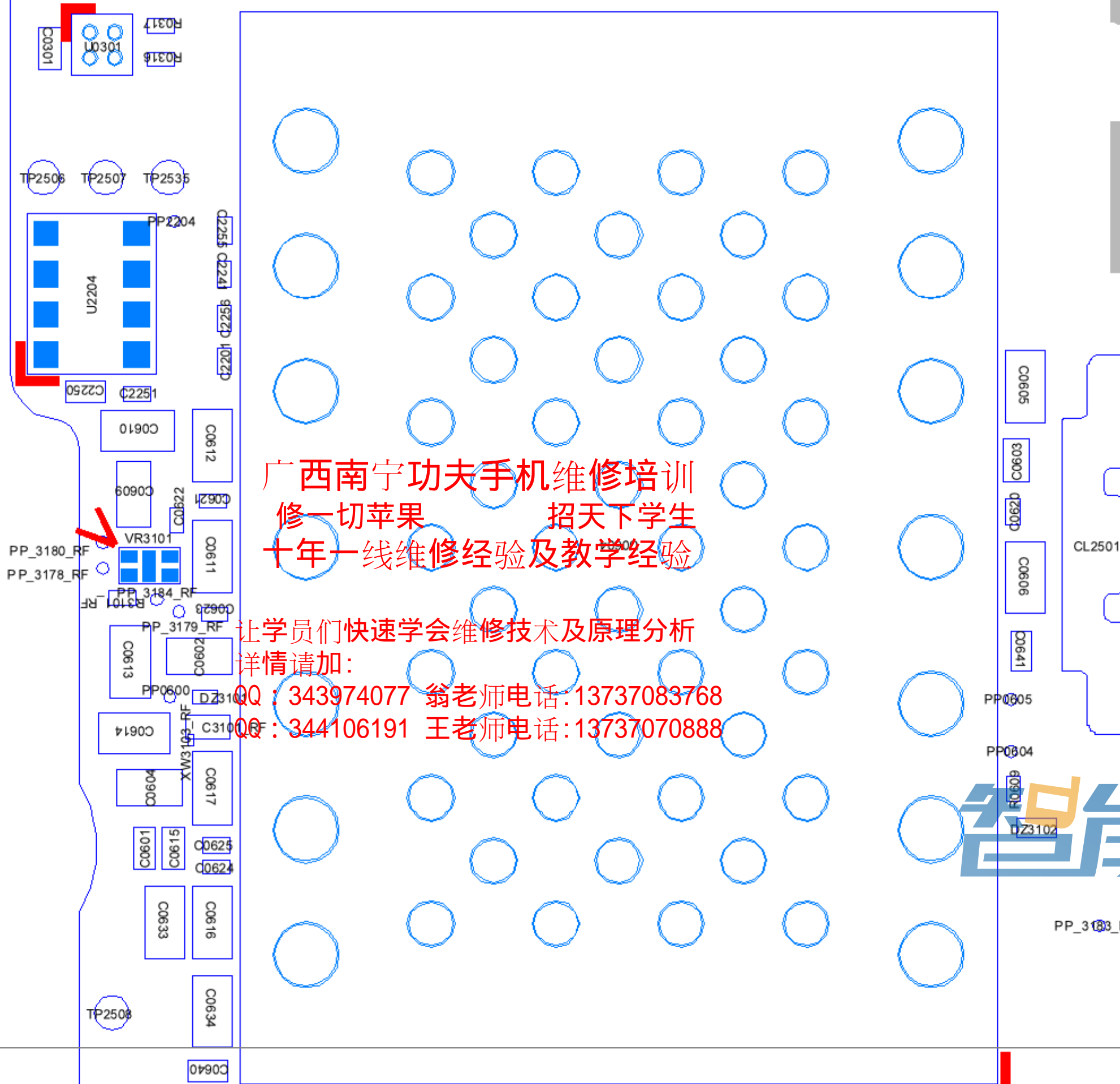
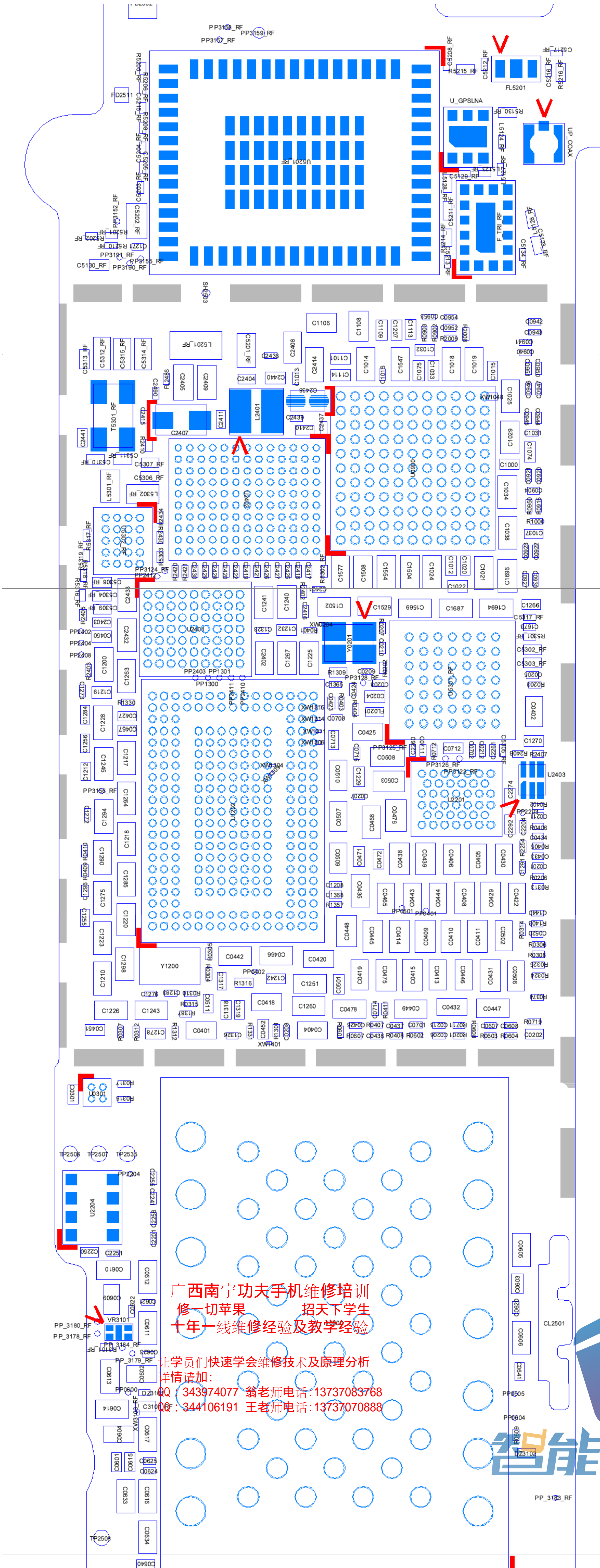


# iphone6 4.7





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1

1. ALL RESISTANCE VALUES ARE IN OHMS, 0.1 WATT +/- 5%.

2. ALL CAPACITANCE VALUES ARE IN MICROFARADS.

3. ALL CRYSTALS & OSCILLATOR VALUES ARE IN HERTZ.

Thu Apr 17 17:11:44 2014

N61 CARRIER BUILD

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N61\_MLB

08/26/2013

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N61\_MLB

08/26/2013

1010

AUDIO:L67 CODEC (2/2)

N61\_MLB

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1111

CAMERA:FRONT FLEX CONN

N61\_MLB

08/26/2013

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POWER:ADI (1/2)

N56\_MLB

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1313

POWER:ADI (2/2)

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1414

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N61\_MLB

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1515

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2121

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2222

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2323

CAMERA:REAR FLEX CONN

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2424

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N/A

N/A

2525

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AP INTERFACE & DEBUG CONNECTORS

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03/24/2014

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BASEBAND PMU (1 OF 2)

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BASEBAND PMU (2 OF 2)

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BASEBAND (1 OF 2)

N61\_RADIO\_MLB

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BASEBAND (1 OF 2)

N61\_RADIO\_MLB

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MOBILE DATA MODEM (2 OF 2)

N61\_RADIO\_MLB

03/24/2014

3637

RF TRANSCEIVER (1 OF 3)

N61\_RADIO\_MLB

03/24/2014

3738

RF TRANSCEIVER (2 OF 3)

N61\_RADIO\_MLB

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3839

RF TRANSCEIVER (3 OF 3)

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QFE DCDC

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LOW BAND PAD

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MID BAND PAD

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HIGH BAND PAD

N61\_RADIO\_MLB

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ANTENNA SWITCH

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HIGH BAND SWITCH

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RX DIVERSITY

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GPS

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GPS

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JUMPER

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03/24/2014

5455

JUMPER

N61\_RADIO\_MLB

03/24/2014

NAND BOM OPTIONS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
335S0998	1	NAND, 19NM, 16GX8, MLC, PPN1.5	U0604	CRITICAL	NAND_16G
335S0993	1	NAND, 19NM, 32GX8, MLC, PPN1.5	U0604	CRITICAL	NAND_32G
335S0994	1	NAND, 19NM, 64GX8, MLC, PPN1.5	U0604	CRITICAL	NAND_64G
335S00010	1	NAND, 19NM, 128GX8, TLC, PPN1.5	U0604	CRITICAL	NAND_128G
138S0867	1	CAP,XSR,10UF,20%,6.3V,0.65MM,HRTZ,0402	C0610,C0611,C0614,C0634	CRITICAL	NAND_16G
138S0867	1	CAP,XSR,10UF,20%,6.3V,0.65MM,HRTZ,0402	C0613,C0633,C0610,C0611,C0614,C0634	CRITICAL	NAND_32G & NAND_64G
138S00003	1	CAP,XSR,15UF,20%,6.3V,0.65MM,HRTZ,0402	C0613,C0633,C0610,C0611,C0614,C0634	CRITICAL	NAND_128G

ALTERNATE NAND BOM OPTIONS

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
335S0992	335S0998	ALTERNATE	J0604	TOSHIBA,NAND,16GB
335S1038	335S0998	ALTERNATE	J0604	HYNIX,NAND,16GB
335S1040	335S0994	ALTERNATE	J0604	HYNIX,NAND,64GB
335S00014	335S0994	ALTERNATE	J0604	TOSHIBA,NAND,64GB
335S00015	335S00010	ALTERNATE	J0604	TOSHIBA,NAND128GB
335S00009	335S0994	ALTERNATE	J0604	SANDISK,NAND,64GB,TLC

SHIELD BOM OPTIONS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
604-00241	1	SUBASSY, SHIELD, UPPER FRONT, N61	SH2501	CRITICAL	COMMON
604-00242	1	SUBASSY, SHIELD, LOWER FRONT, N61	SH2502	CRITICAL	COMMON
604-00243	1	SUBASSY, SHIELD, LOWER BACK, N61	SH2504	CRITICAL	COMMON
604-00244	1	SUBASSY, SA SHIELD, N61	SH2506	CRITICAL	COMMON

N61 BOM CALLOUTS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
051-9903	1	SCH, MLB, N61	SCH	CRITICAL	?
820-3486	1	PCBF, MLB, N61	PCB	CRITICAL	?
825-6838	1	EEEE FOR 639-4237 16GB	EEEE_G16T	CRITICAL	EEEE_16G
825-6838	1	EEEE FOR 639-5838 32GB	EEEE_G16R	CRITICAL	EEEE_32G
825-6838	1	EEEE FOR 639-5839 64GB	EEEE_G16Q	CRITICAL	EEEE_64G
825-6838	1	EEEE FOR 639-00025 128GB	EEEE_G16N	CRITICAL	EEEE_128G
825-6838	1	EEEE FOR 639-00208 16GB	EEEE_F98F	CRITICAL	EEEE_16G_TDDLTE
825-6838	1	EEEE FOR 639-00209 32GB	EEEE_FQK0	CRITICAL	EEEE_32G_TDDLTE
825-6838	1	EEEE FOR 639-00210 64GB	EEEE_FQJY	CRITICAL	EEEE_64G_TDDLTE
825-6838	1	EEEE FOR 639-00212 128GB	EEEE_FY9W	CRITICAL	EEEE_128G_TLC_TDDLTE

ALTERNATE BOM OPTIONS

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
152S1844	152S1836	ALTERNATE	L1604	TY ALT INDUCTOR
152S1842	152S1849	ALTERNATE	L1519	TY ALT INDUCTOR
197S0392	197S0369	ALTERNATE	Y1200	ESRON ALT XTAL
197S0399	197S0369	ALTERNATE	Y1200	NDK ALT XTAL
338S1285	338S1202	ALTERNATE	U1601	L21 SPKAMP
152S2034	152S2033	ALTERNATE	L1209,L1211,L1213	1.2MM 1.0UH, CYNTEC
152S00004	152S2049	ALTERNATE	L1210,L1212,L1214	1.2MM 0.47UH, CYNTEC
339S00005	339S0246	ALTERNATE	U0201	FIJI, B0, SAMSUNG
339S0247	339S0246	ALTERNATE	U0201	FIJI, B0, HYNIX
339S00006	339S0246	ALTERNATE	U0201	FIJI, B1, E
339S00007	339S0246	ALTERNATE	U0201	FIJI, B1, H
339S00008	339S0246	ALTERNATE	U0201	FIJI, B1, S
155S0773	155S0453	ALTERNATE		TY 120OHM FERRITE
118S0764	118S0717	ALTERNATE	R1309	3.92KOHM, 01005
343S0688	343S0638	ALTERNATE	U2401	CUMULUS C1, FAB4
138S00005	138S00003	ALTERNATE	C1290	15UF,0402,HRTZL CAP
155S00011	155S00008	ALTERNATE	L1135	CMC,90OHM,MURATA
377S0168	377S0140	ALTERNATE	DZ1113	SUPPL TRANS,VARIABLE,AMOTECH
155S0885	155S0610	ALTERNATE	FL1802,FL1803	FERR RD,150OHM,100MA,01005
138S0648	138S0652	ALTERNATE	C1018	CAP,4.7UF,20%,6.3V,0402,R=0.65MM
138S0657	138S0702	ALTERNATE	C1106	CAP,4.3UF,20%,4V,0410
338S00028	338S00017	ALTERNATE	U2203	CARBON, BOSCH, BMT162BC
338S00029	338S00017	ALTERNATE	U2203	CARBON, ST, AP6DE2AA
335S00013	335S0894	ALTERNATE	J0301	BT SK ESPRON

SCH 051-9903

BRD 820-3486

MCO 056-6825

BOM 639-4237 (16GB,BETTER)

BOM 639-5838 (32GB,BEST)

BOM 639-5839 (64GB,ULTRA)

BOM 639-00208 (16GB,BETTER,DTD)

BOM 639-00209 (32GB,BEST,DTD)

BOM 639-00210 (64GB,ULTRA,DTD)

BOM 639-00025(128GB,SUPREME,TLC)

BOM 639-00212(128GB,SUPREME,TLC,DTD)

DRAWING TITLE

SCHEM,MLB,N61

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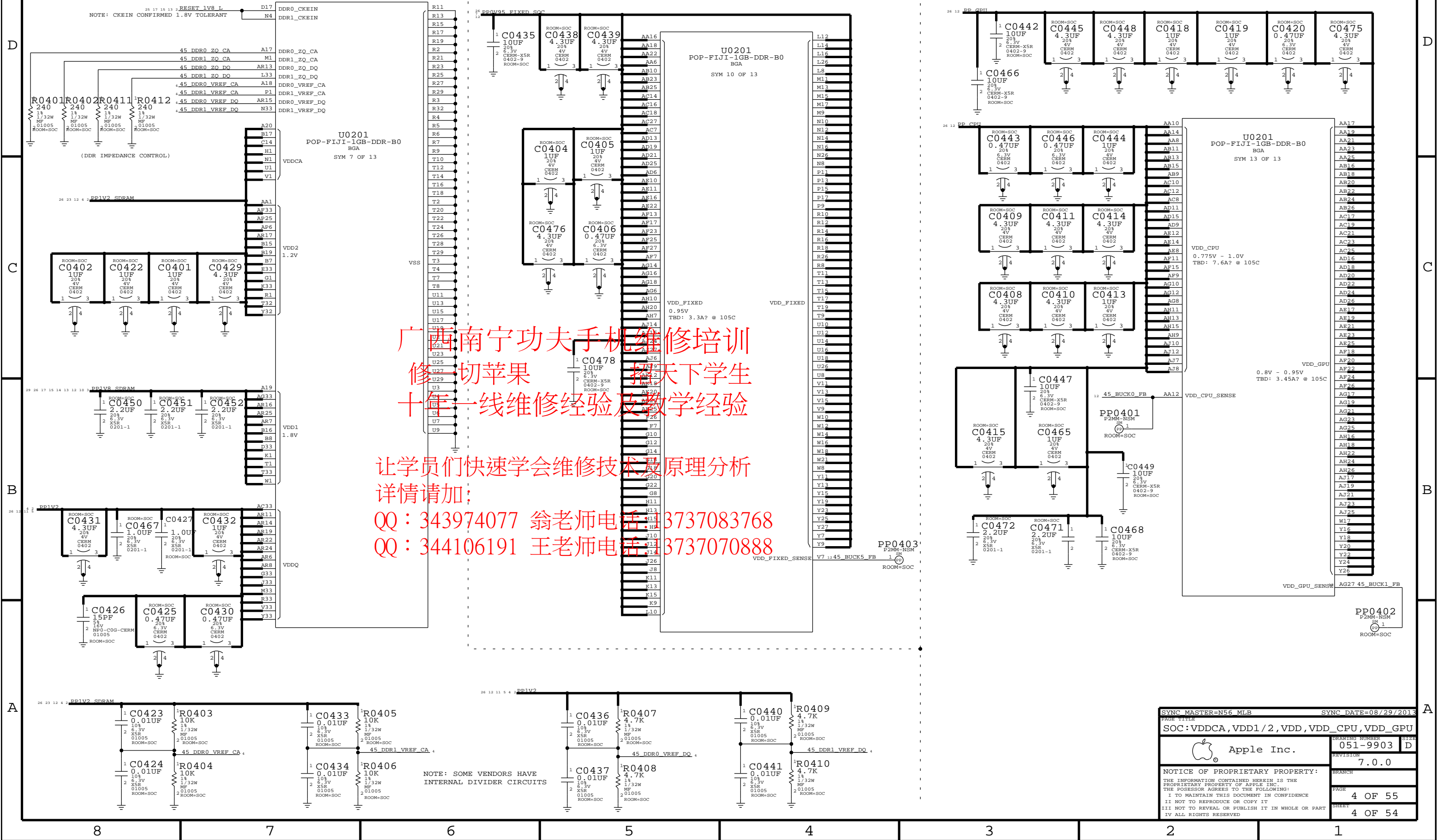
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# FIJI: VDDCA, VDD1/2, VDDQ, VDD, VDD\_FIXED, VDD\_CPU, VDD\_GPU

VDDCA, VDD1/2, VDDQ

VDD

VDD\_CPU, VDD\_GPU

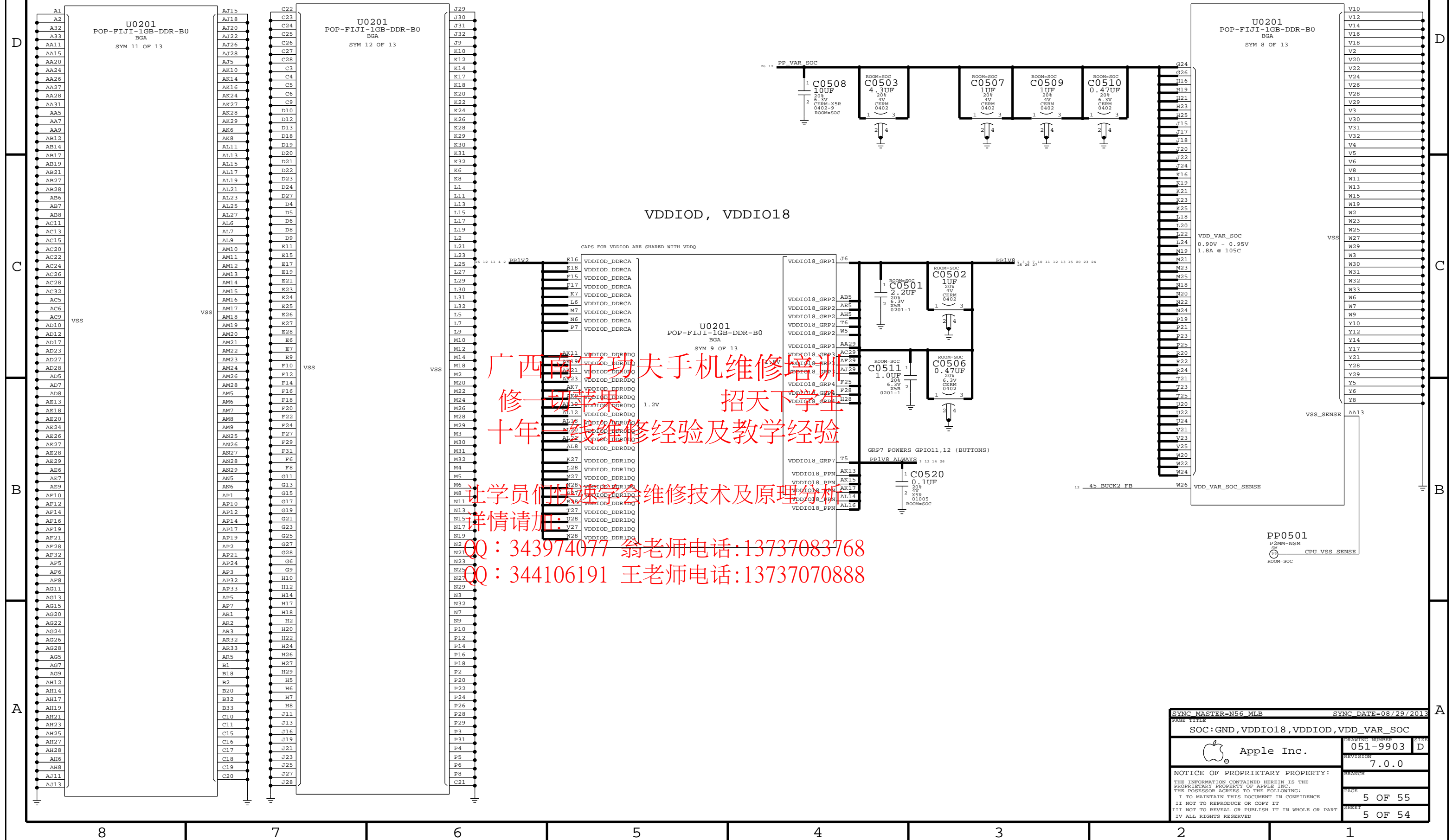




```
FIJI:  VDDIOD,VDDIO18,VDD_VAR_SOC
```

JUST A FEW GNDS

VDD\_SRAM, VDD\_SOC



4	AK11	VDDIOD_DDRDQ	VDDIO18_GRP3
7	AK19	VDDIOD_DDRDQ	VDDIO18_GRP1
8	AK21	VDDIOD_DDRDQ	VDDIO18_GRP1
9	AK23	VDDIOD_DDRDQ	VDDIO18_GRP1
0	AK7	VDDIOD_DDRDQ	VDDIO18_GRP4
2	AK9	VDDIOD_DDRDQ	VDDIO18_GRP4
3	AK10	VDDIOD_DDRDQ	VDDIO18_GRP4
4	AK14	VDDIOD_DDRDQ	VDDIO18_GRP4
6	AK12	VDDIOD_DDRDQ	VDDIO18_GRP4
7	AK16	VDDIOD_DDRDQ	VDDIO18_GRP4
8	AK17	VDDIOD_DDRDQ	VDDIO18_GRP4
9	AK22	VDDIOD_DDRDQ	VDDIO18_GRP4
0	AK24	VDDIOD_DDRDQ	VDDIO18_GRP4

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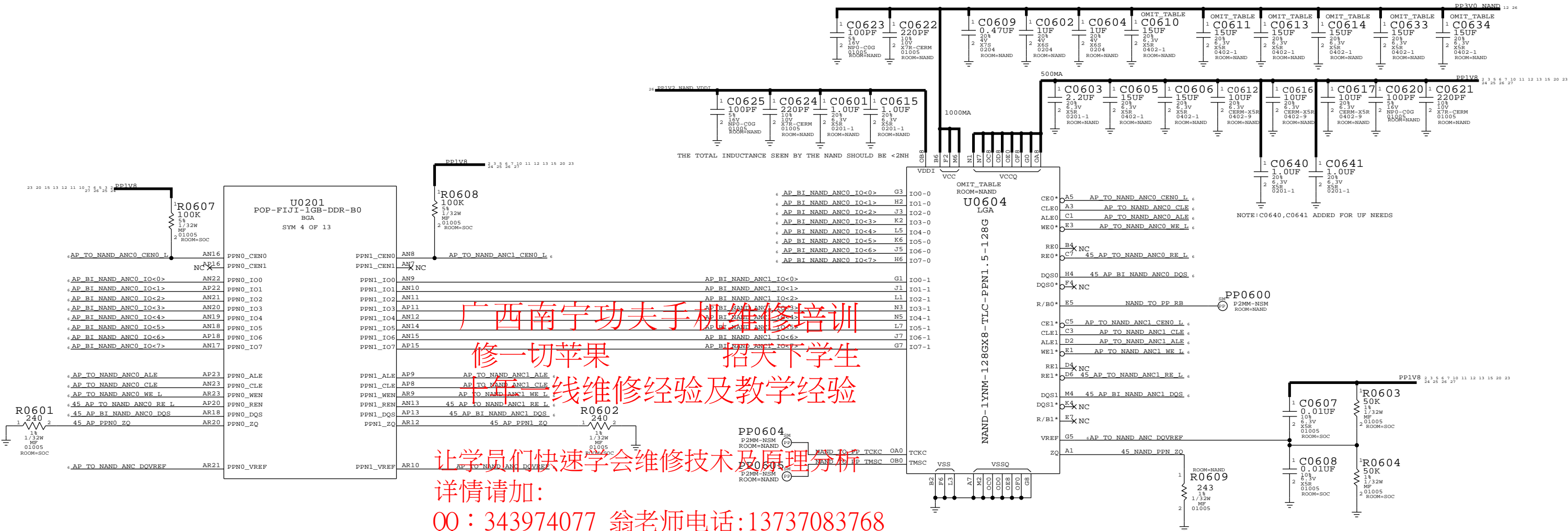
3 T27 VDDIOD\_DDR1DQ VDDIO18\_PPN  
 5 详情请加: U28 VDDIOD\_DDR1DQ  
 7 V27 VDDIOD\_DDR1DQ

QQ: 343974077 翁老师电话: 137370837

QQ: 344106191 王老师电话: 137370708

# FIJI: NAND + 12X17 NAND PKG

SUPPORT FOR PPN1.5 (1.8V IO) ONLY




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PP0601 P4MM NOTE: IO<6> PREFERRED BY MATT BYOM (N51) (IS A STATUS READY BIT)  
ROOM=SOC 1 AP BI NAND ANCO IO<6>  
PP0602 P4MM 1 45 AP TO NAND ANCO RE L  
ROOM=SOC 1 45 AP TO NAND ANCO RE L  
PP0603 P4MM 1 45 AP BI NAND ANCO DQS  
ROOM=SOC 1 45 AP BI NAND ANCO DQS

SYNC MASTER=N56 MLB		SYNC DATE=08/29/2013	
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SOC : NAND			
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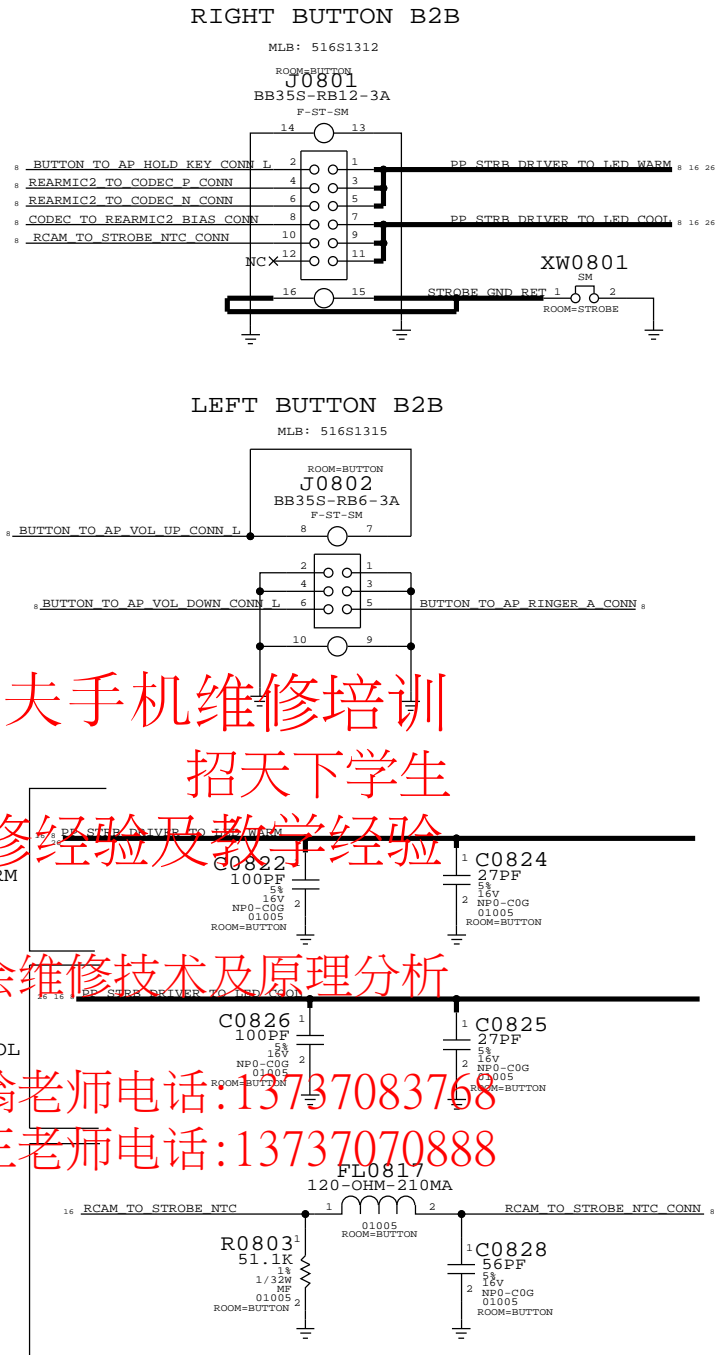
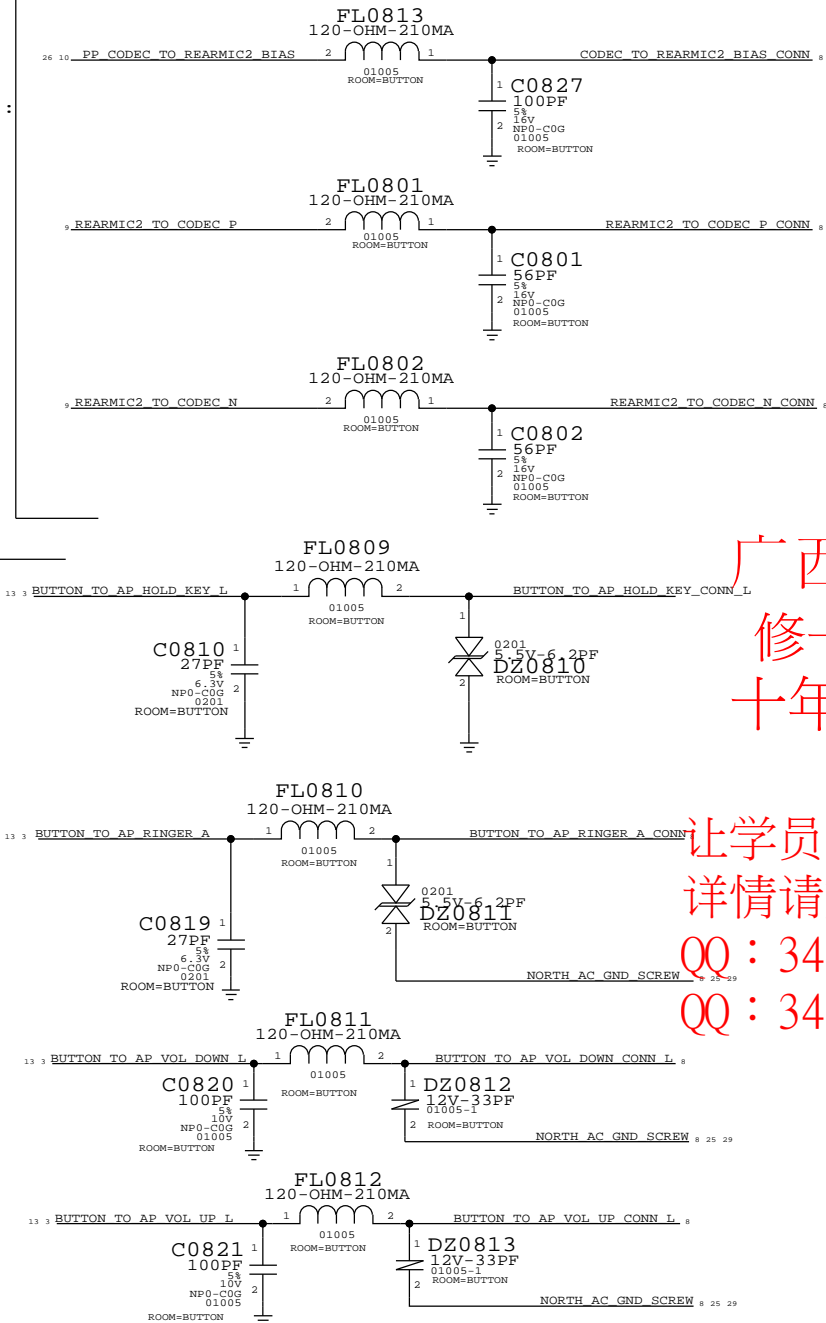


8 7 6 5 4 3 2 1

# BUTTON FLEX (BUTTONS, ANC REF MIC, STROBE, STROBE\_NTC, WIFI FLEX PAC)


MIC2 (ANC REF MIC):  
MIC2/4 BIAS,  
MIC2\_P,\_N

BUTTONS:  
RINGER, HOLD,  
VOL\_UP/DOWN,



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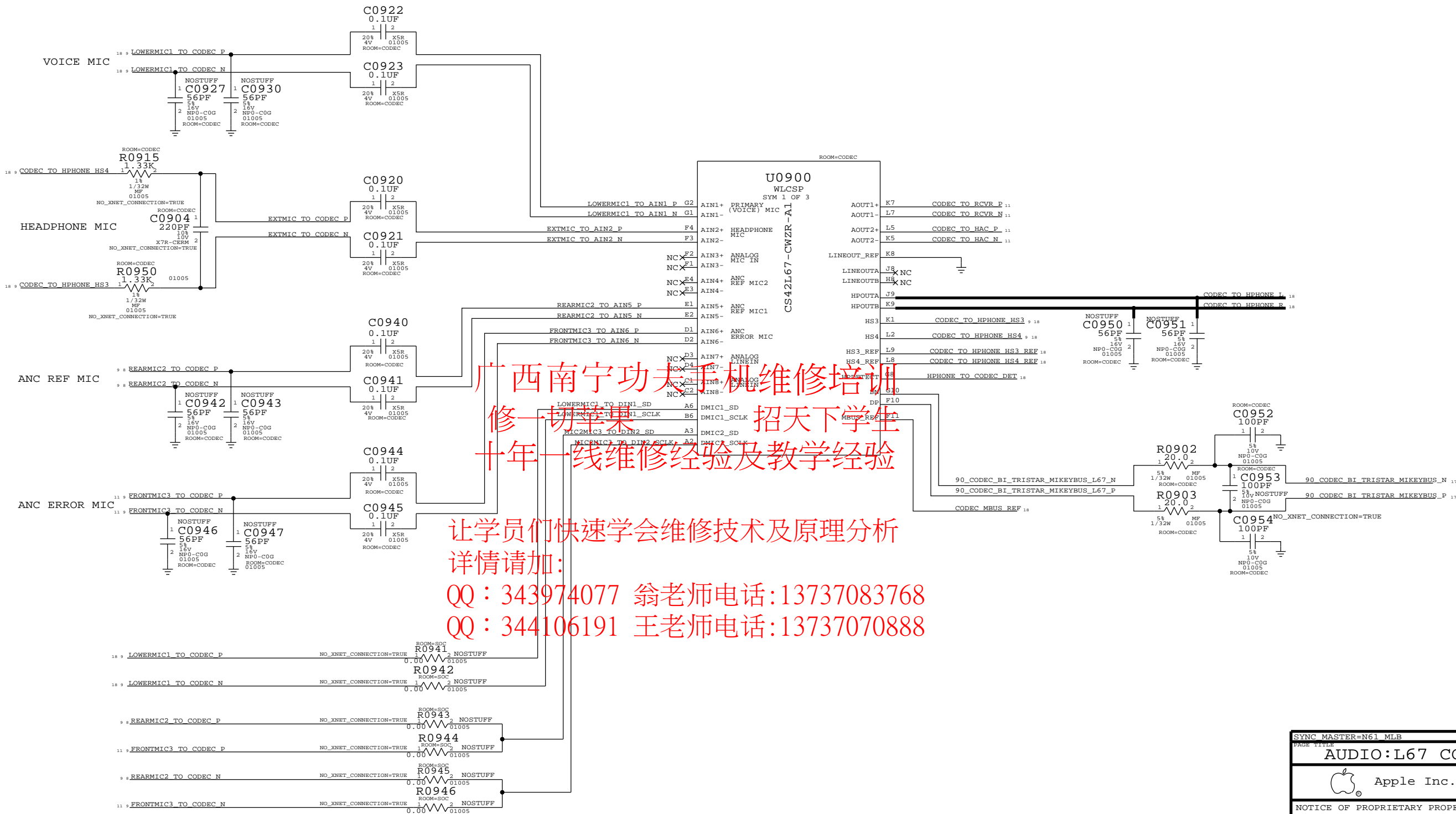
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IO:BUTTON FLEX CONN			
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# L67 AUDIO CODEC


## AUDIO I/O

(ANALOG MIC IN, DIG MIC IN, HPOUT, LINEOUT, RECEIVER OUT, MIKEYBUS)



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AUDIO:L67 CODEC (1/2)			
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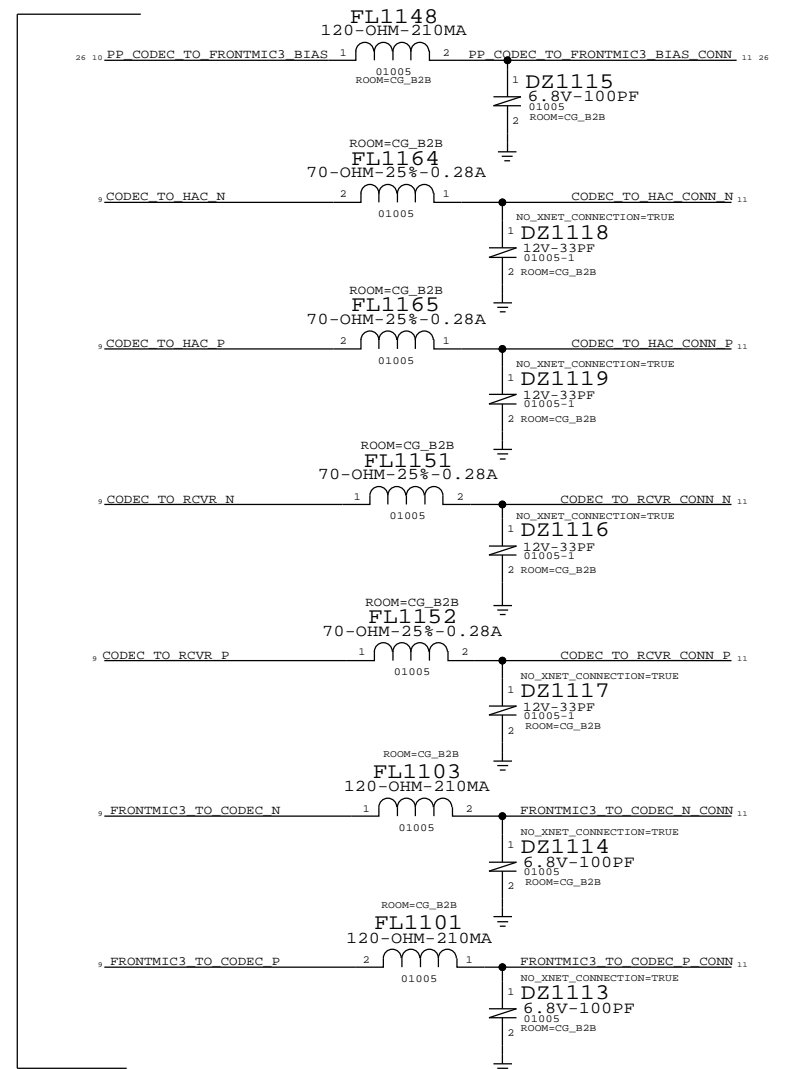
CS42L67-  
+VCP\_FILT K11 25 PP\_CODEC VCPFILT- XW1048 2 20V  
GNDP\_FILT L10 25 PP\_CODEC VCPFILT- 1 2 20V  
GNDP\_CPL L10 25 PP\_CODEC VCPFILT- 1 2 20V  
-VCP\_FILT L10 25 PP\_CODEC VCPFILT- 1 2 20V  
SPEAKER\_VO J7 1 PP\_CODEC SPEAKER\_VO ROOM=CODEC 1 C1034 4.7UF  
GNDP K6 4.7UF  
H11 PP\_CODEC VCPFILT- 1 2 20V  
H12 PP\_CODEC VCPFILT- 1 2 20V  
H2 C1034 4.7UF  
RES1 TR15 CAP AN CODEC DINS


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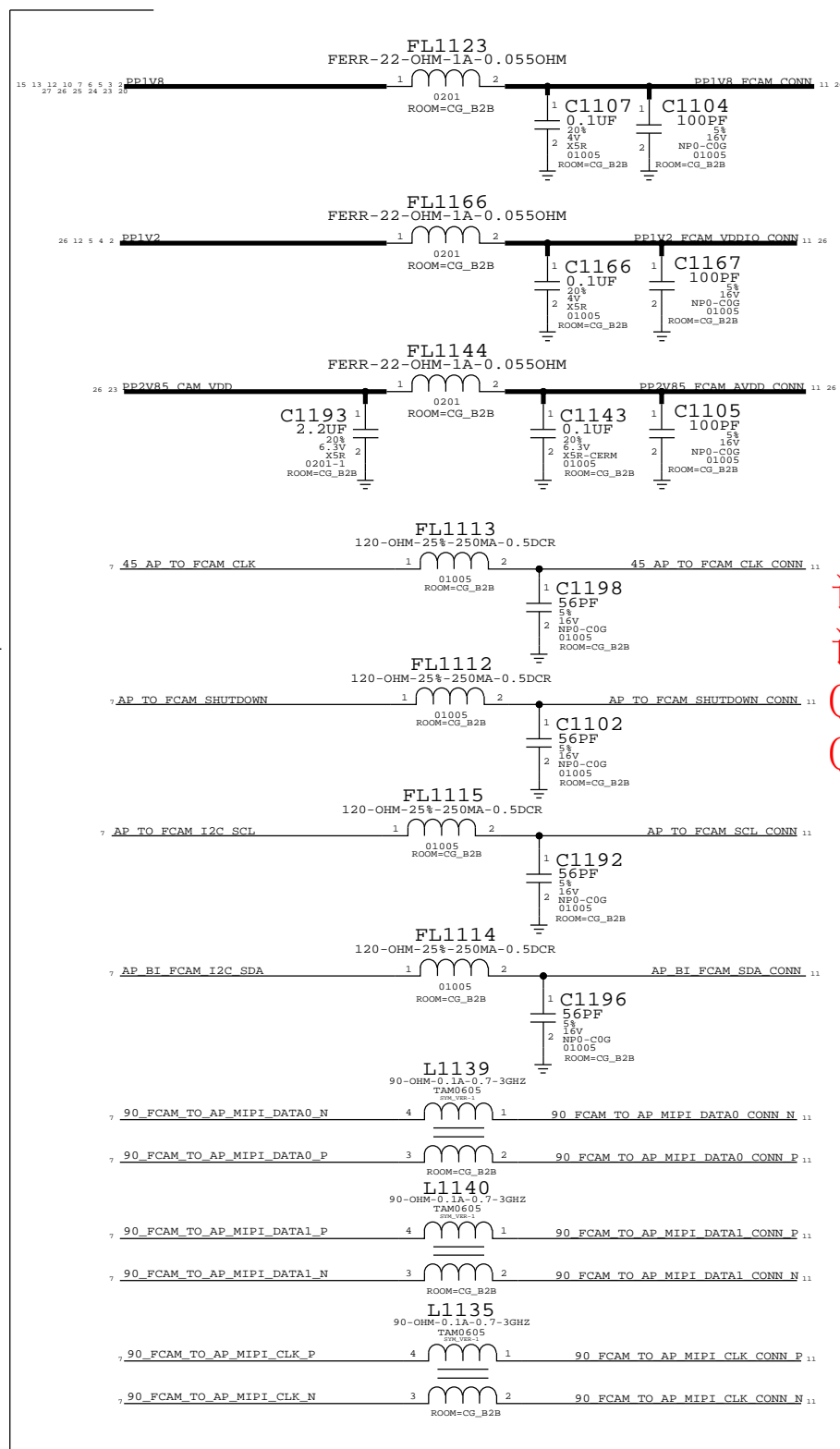
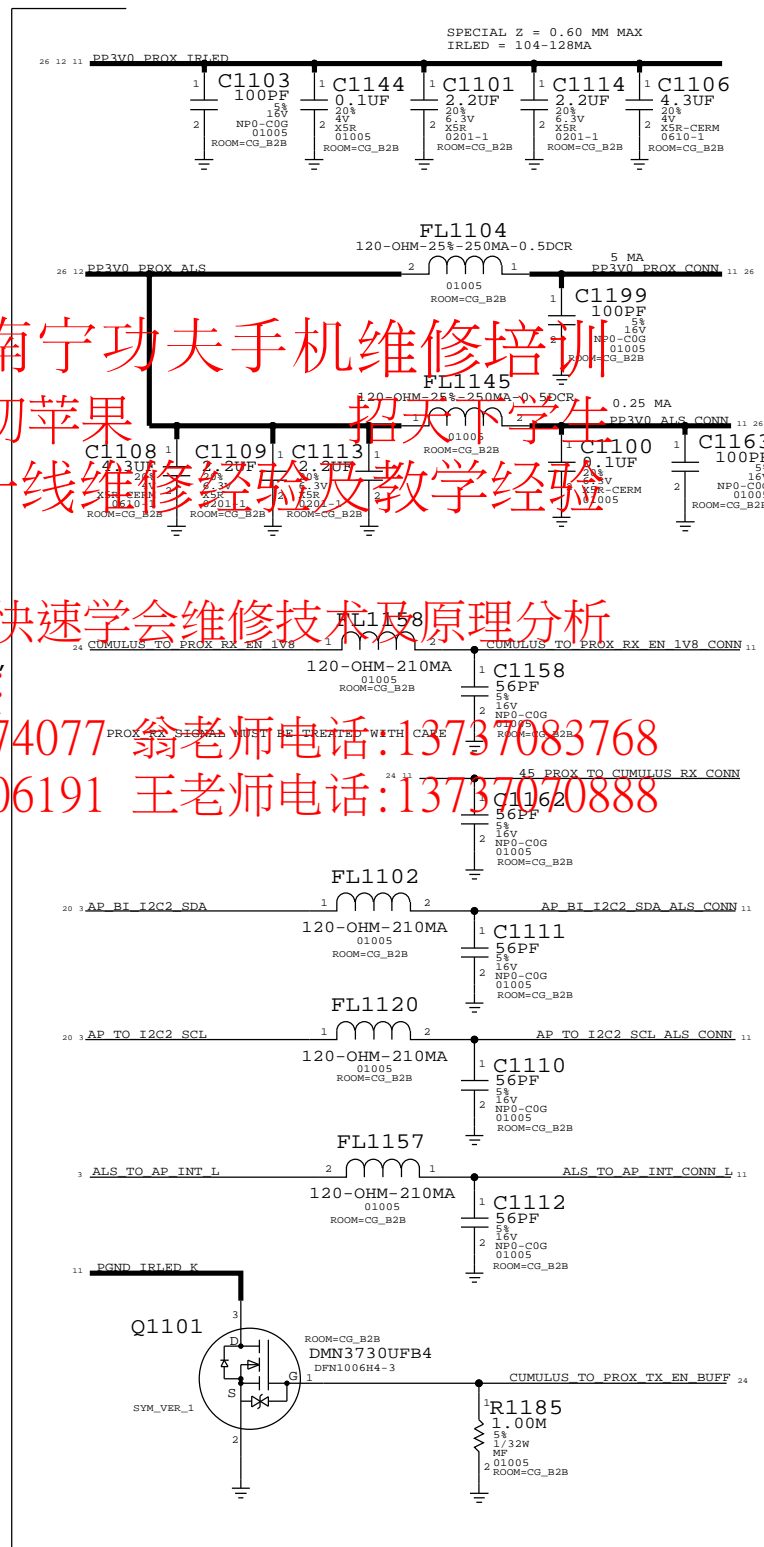
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QQ: 344106191 王老师电话: 13737070888

## (FCAM, PROX, ALS, RECEIVER, ANC ERROR MIC)



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CAMERA : FRONT FLEX CONN			
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十年一线维修经验女教学老师

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QQ：344106191 王老师电话：13737708888

## AUDIO



## D



C

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D

C

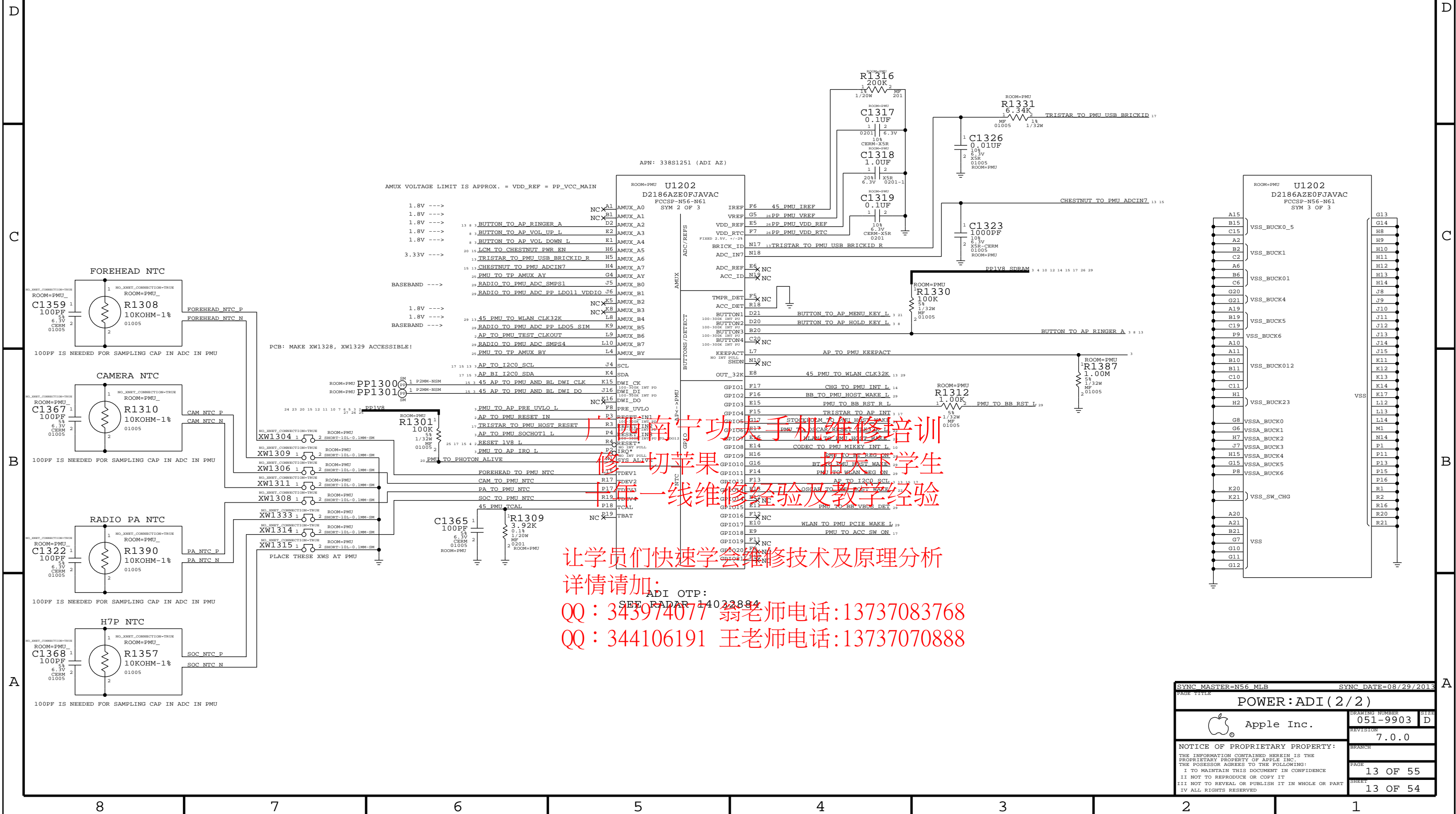
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QQ:324106191 主老师电话:13737070888

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```
(AMUX, GPIO, BUTTONS, ADC, THERMISTORS, SYSTEM I/F, GND)
```



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		PAGE	13 OF 55
		SHEET	13 OF 54



## D



C

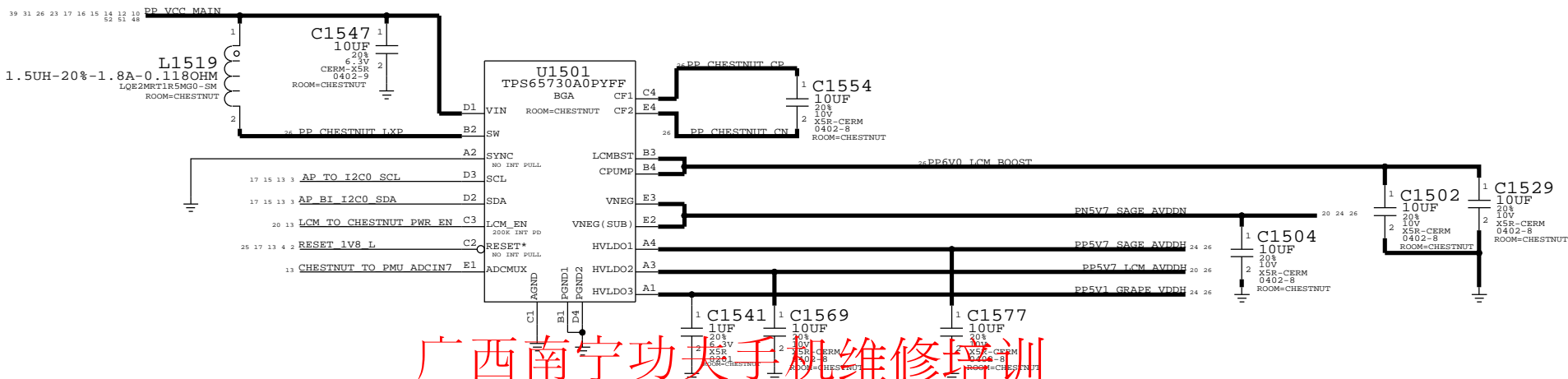
B

B

A

CHESTNUT, BACKLIGHT DRIVER, MESA BOOST

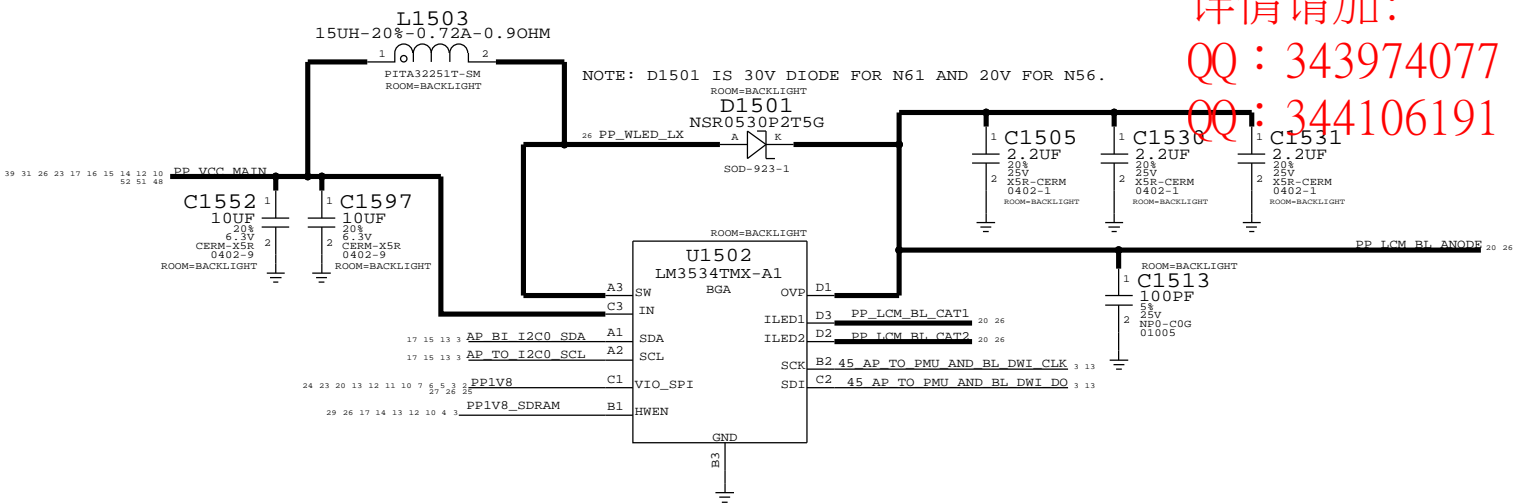
D500 DISPLAY PMU (TI CHESTNUT, 338S1149)



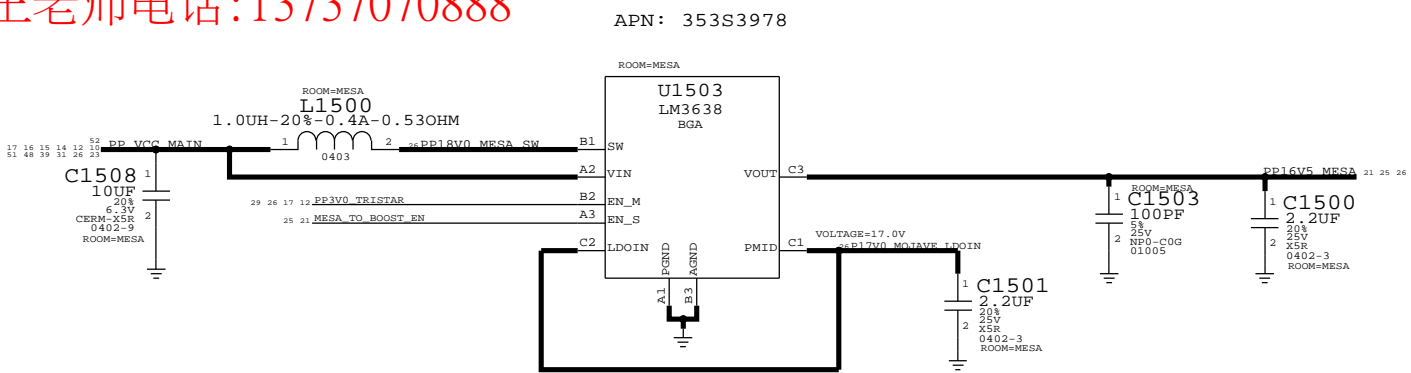
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D500 BACKLIGHT DRIVER

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MESA BOOST A0



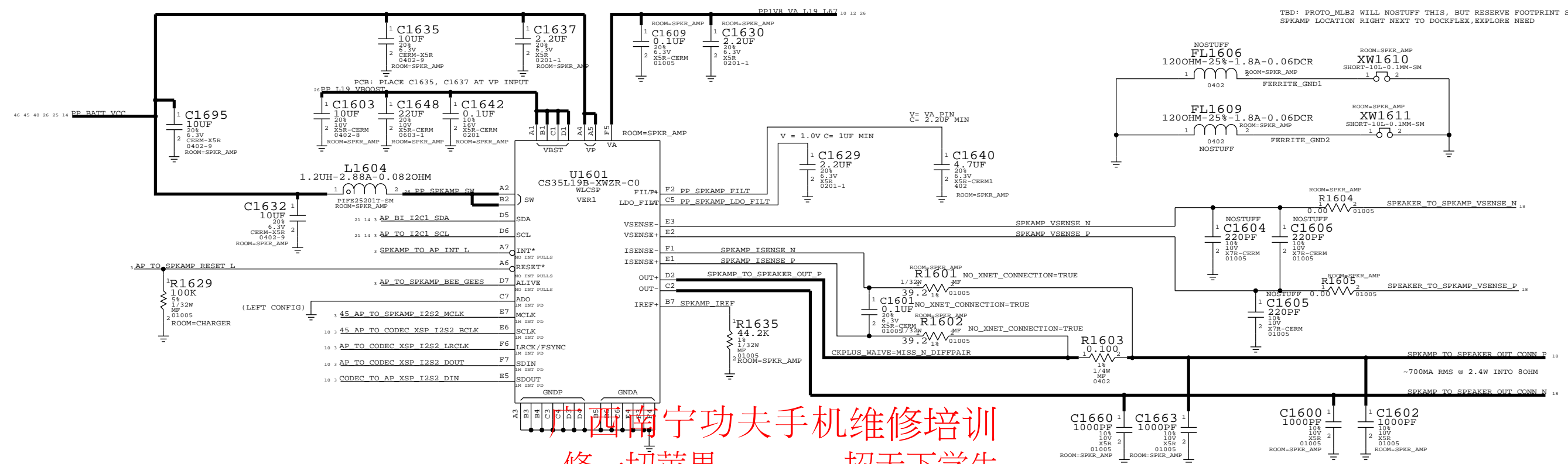
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051-9903		051-9903	
REVISION		REVISION	
7.0.0		7.0.0	
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SPEAKER AMP, LED DRIVER

SPEAKER AMP

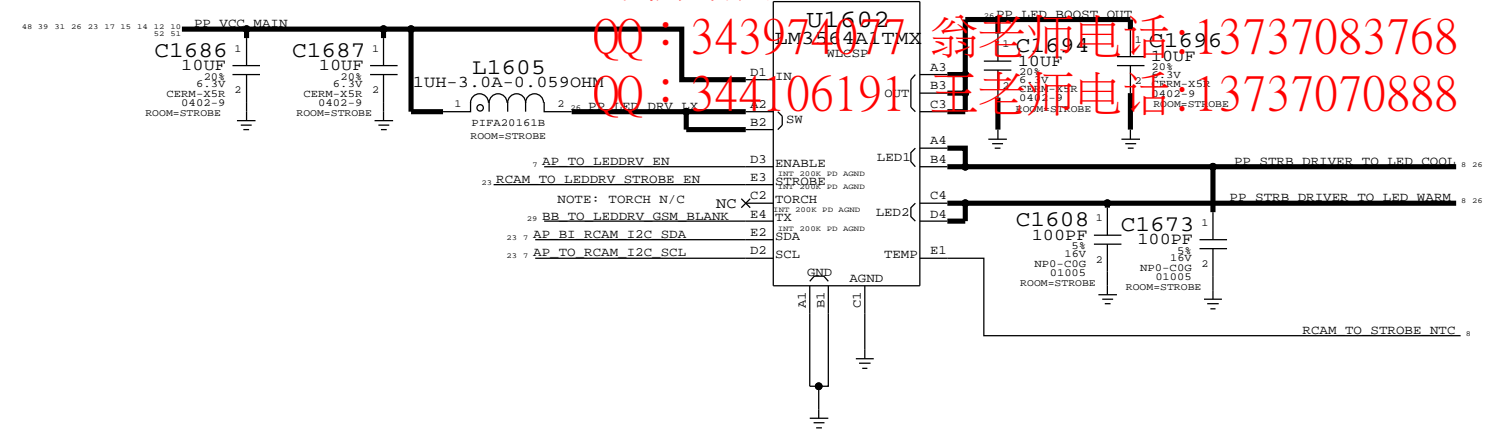
I2C ADDRESS: 1000000X




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STROBE DRIVER

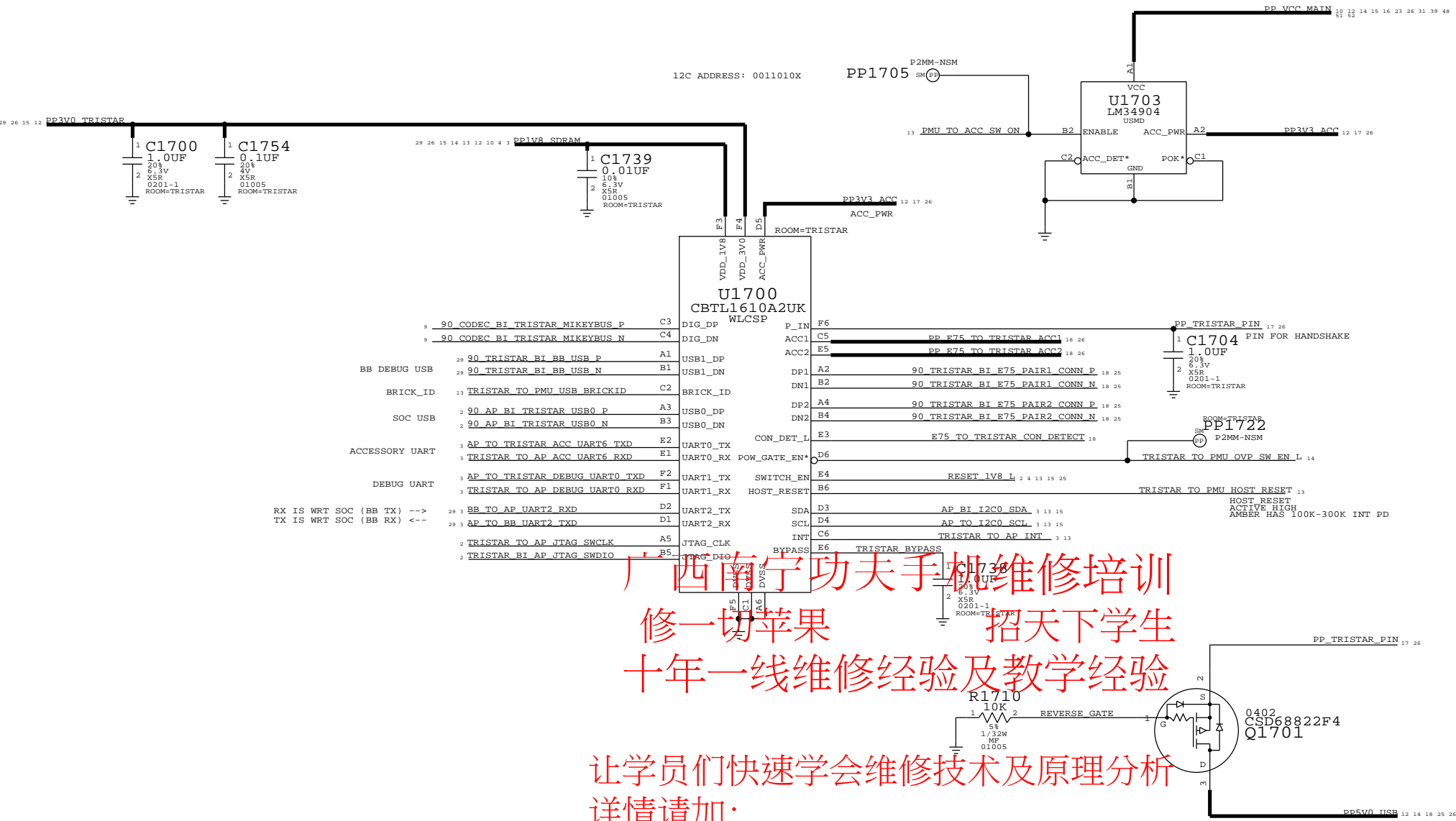
TI: APN 352S3899

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SYNC MASTER=N61 MLB		SYNC DATE=08/26/2013	
PAGE TITLE			
AUDIO:SPKR AMP,STROBE			
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TRISTAR2



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DOCKFLEX B2B (USB VBUS, SPEAKER, ANTENNA LAT SW CTRL, MIC1 (PRIMARY MIC), ACC DET/ID/PWR, E75 DIFFPAIRS)

D

LOWER MIC1  
(PRIMARY  
VOICE MIC)

C

HEADPHONE

B

CODEC TO  
HEADPHONE

A

ACCESSORY:  
VIBE  
DRIVE

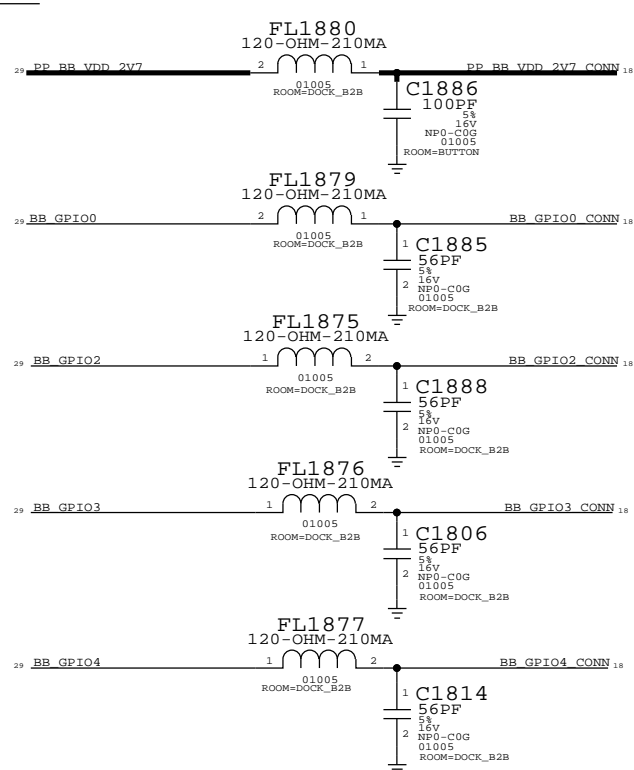
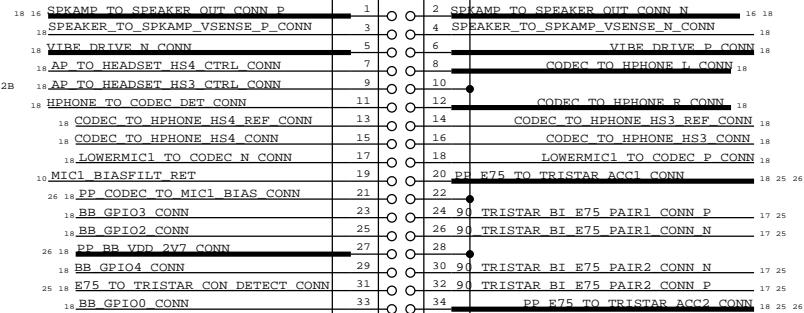
SPEAKER:  
LEADS,  
VSENSE

TRISTAR

USB  
VBUS

ANTENNA

MLB: 516S1281 (RCPT)  
ROOM=DOCK\_B2B J1817  
24-5859-036-201-829  
F-ST-SM



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IO:DOCK FLEX CONN		DRAWING NUMBER	051-9903
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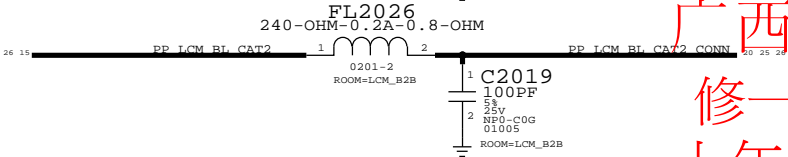
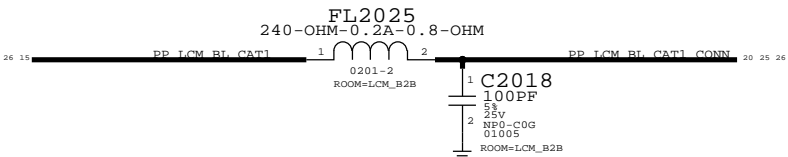
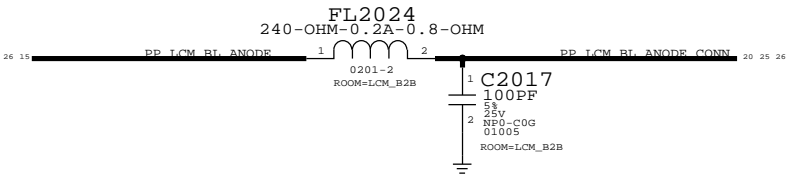
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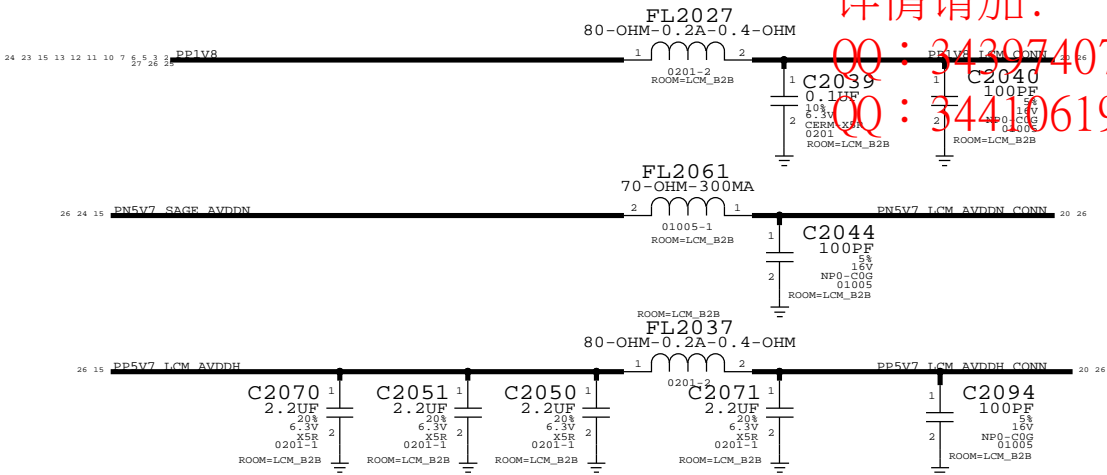
# LCD B2B

## Backlight

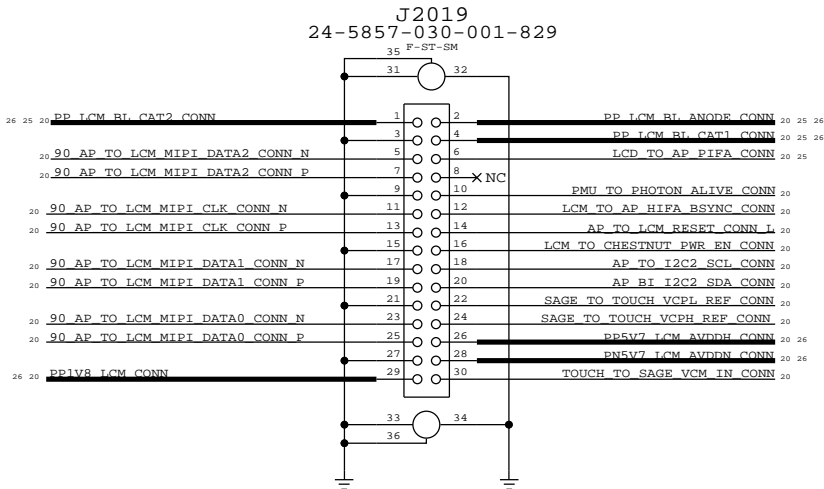
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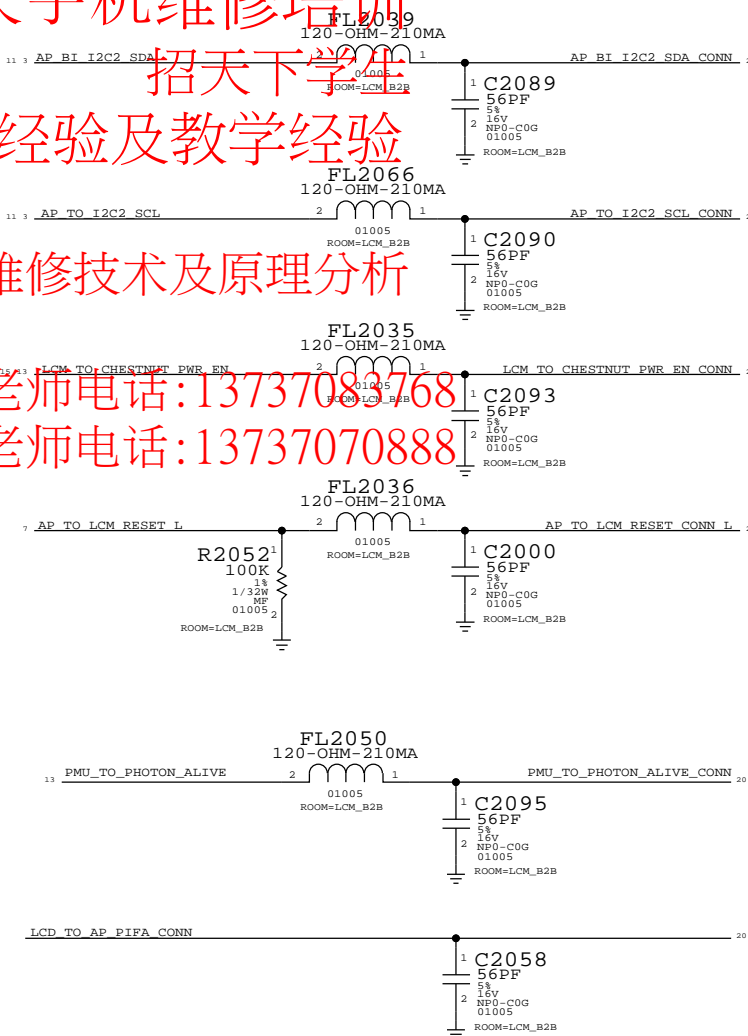
## LCM Supplies



THIS ONE ON MLB ----> 516S1164

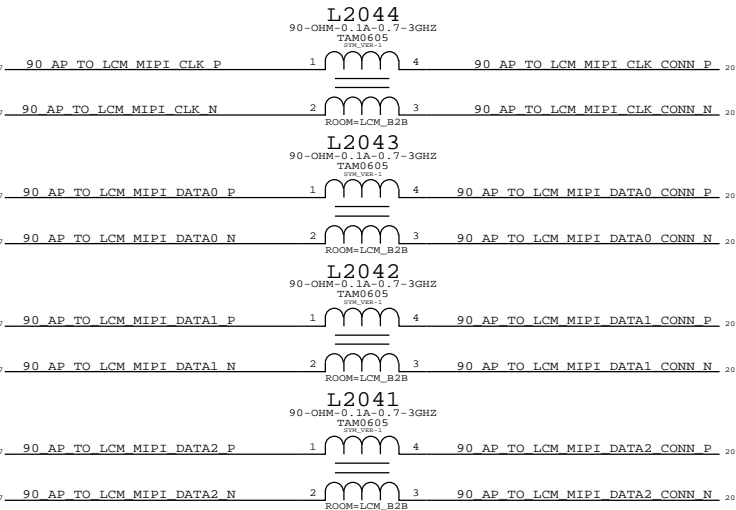


## Digital Interfaces

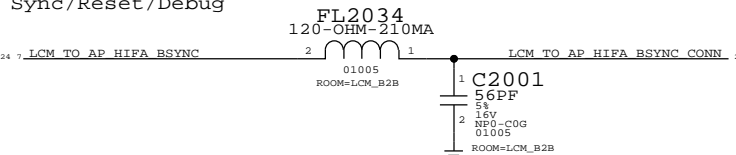


## MIPI Common Mode Chokes

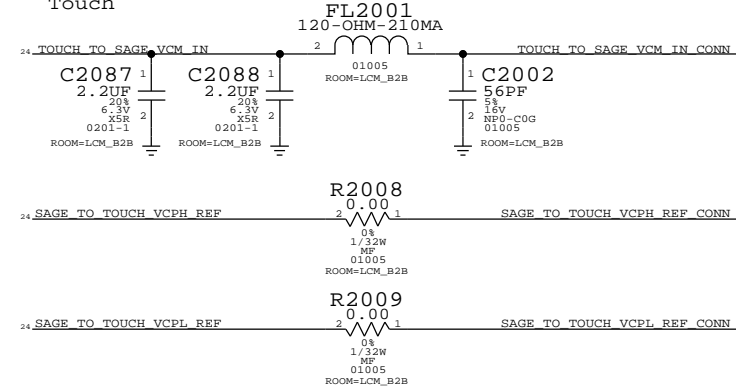
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## Sync/Reset/Debug




## Touch



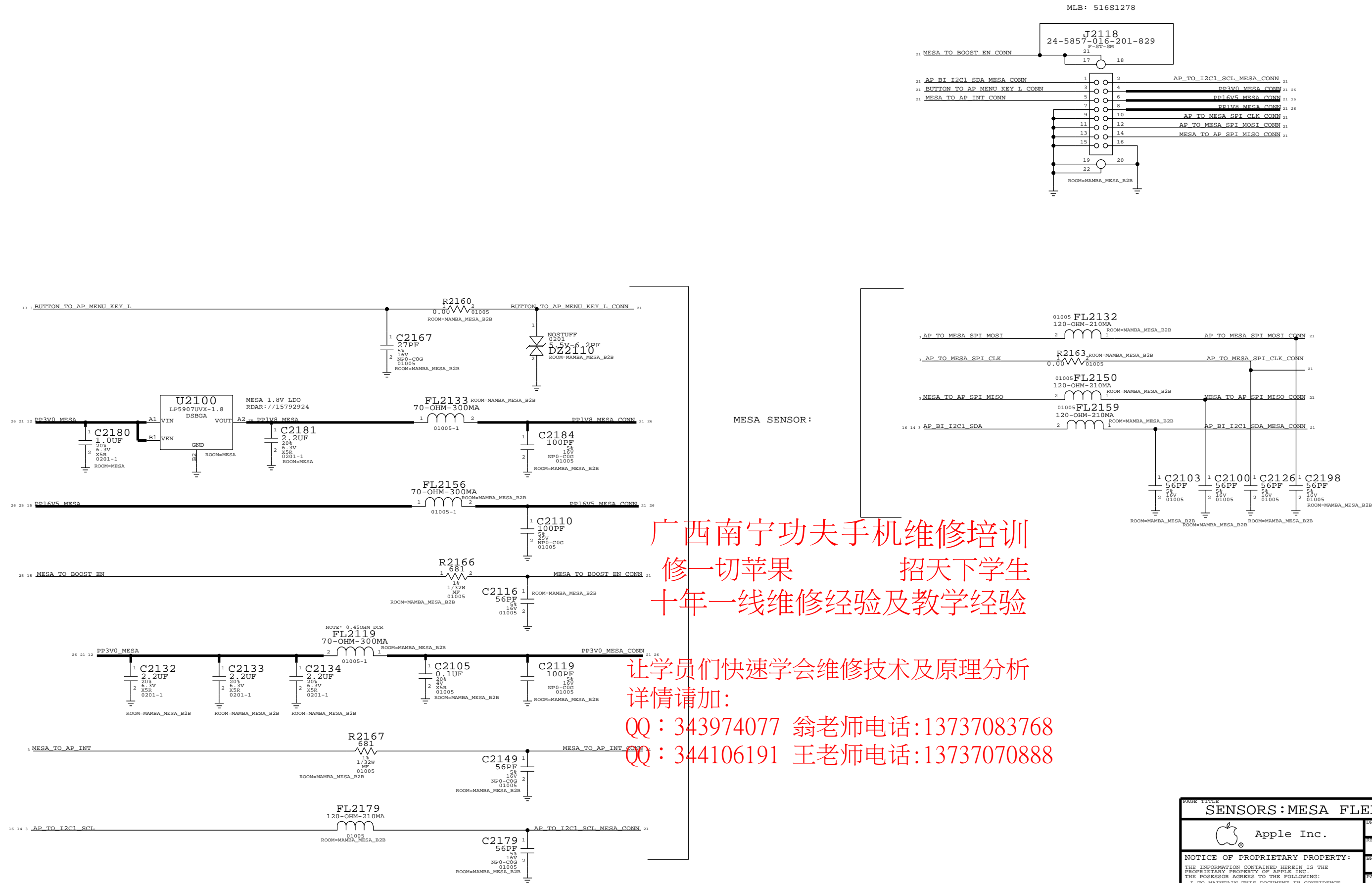
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SYNC MASTER=N61 MLB		SYNC DATE=08/26/2013	
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MESA CONNECTOR

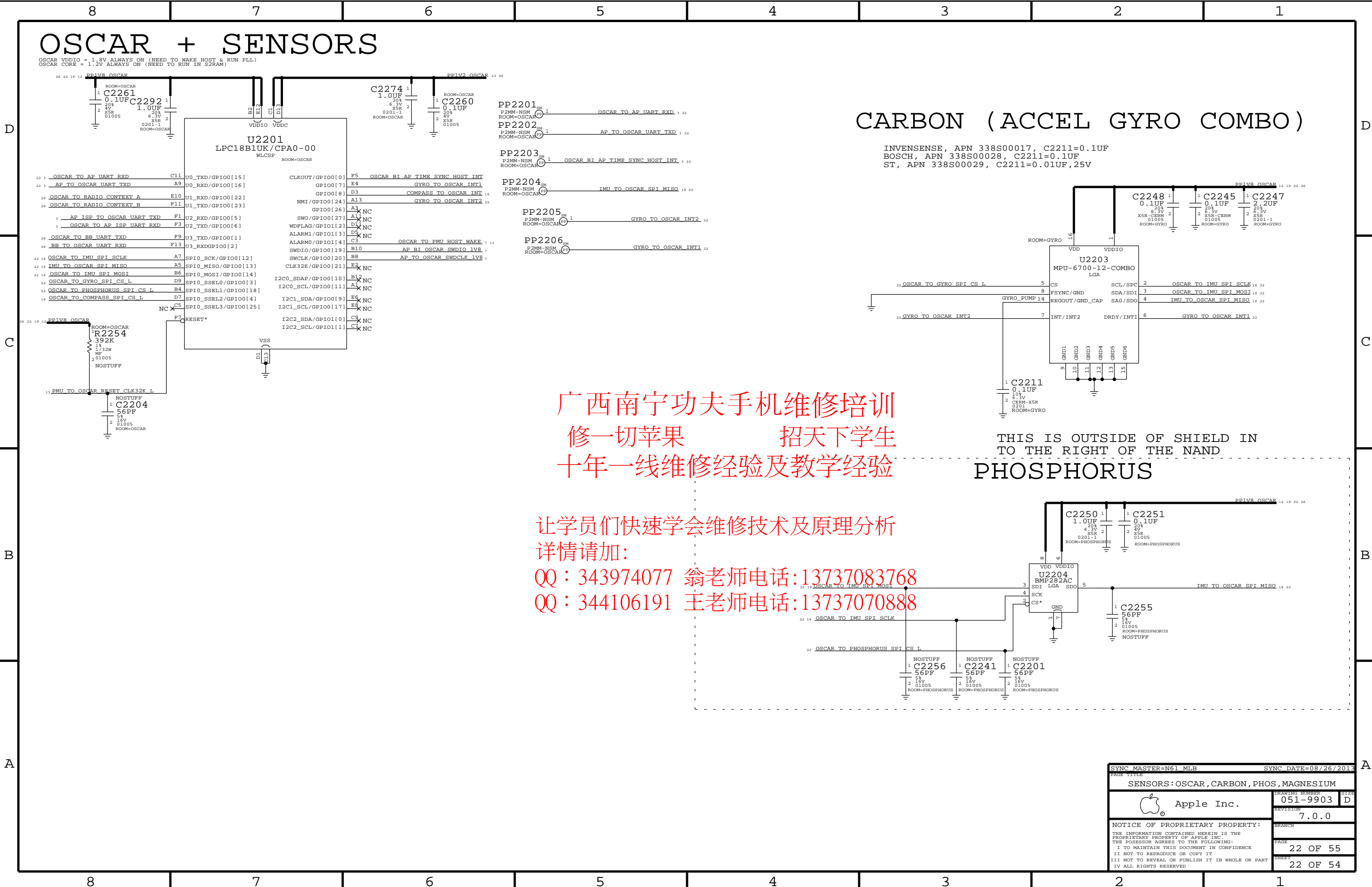


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		SHEET 21 OF 54





# OSCAR + SENSORS

OSCAR VDDIO = 1.8V ALWAYS ON (NEED TO MAKE HOST & RUN PLL)  
OSCAR CORE = 1.2V ALWAYS ON (NEED TO RUN IN SDRAM)


# CARBON (ACCEL GYRO COMBO)

INVENSENSE, APN 338S00017, C2211=0.1UF  
BOSCH, APN 338S00028, C2211=0.1UF  
ST, APN 338S00029, C2211=0.01UF,25V

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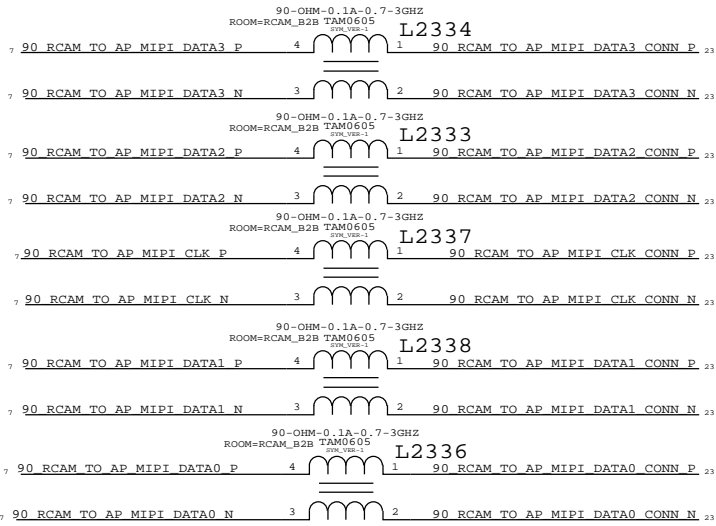
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THIS IS OUTSIDE OF SHIELD IN  
TO THE RIGHT OF THE NAND  
PHOSPHORUS

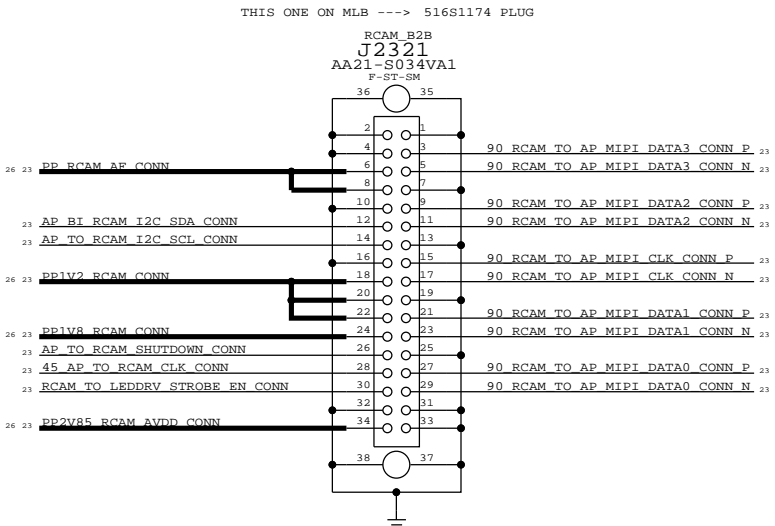
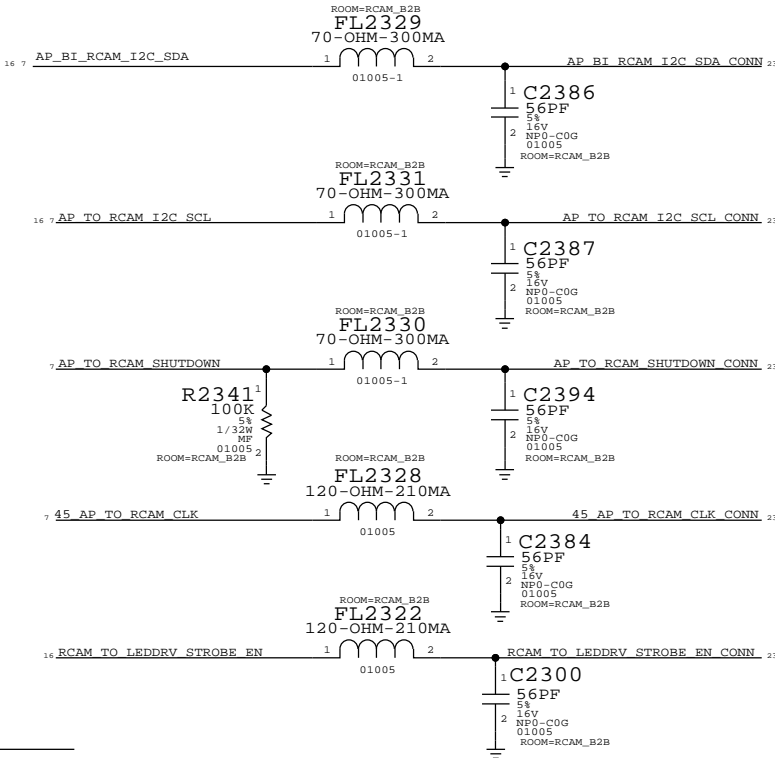
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SENSORS: OSCAR, CARBON, PHOS, MAGNESIUM			
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RCAM B2B (REAR CAMERA CONNECTOR)

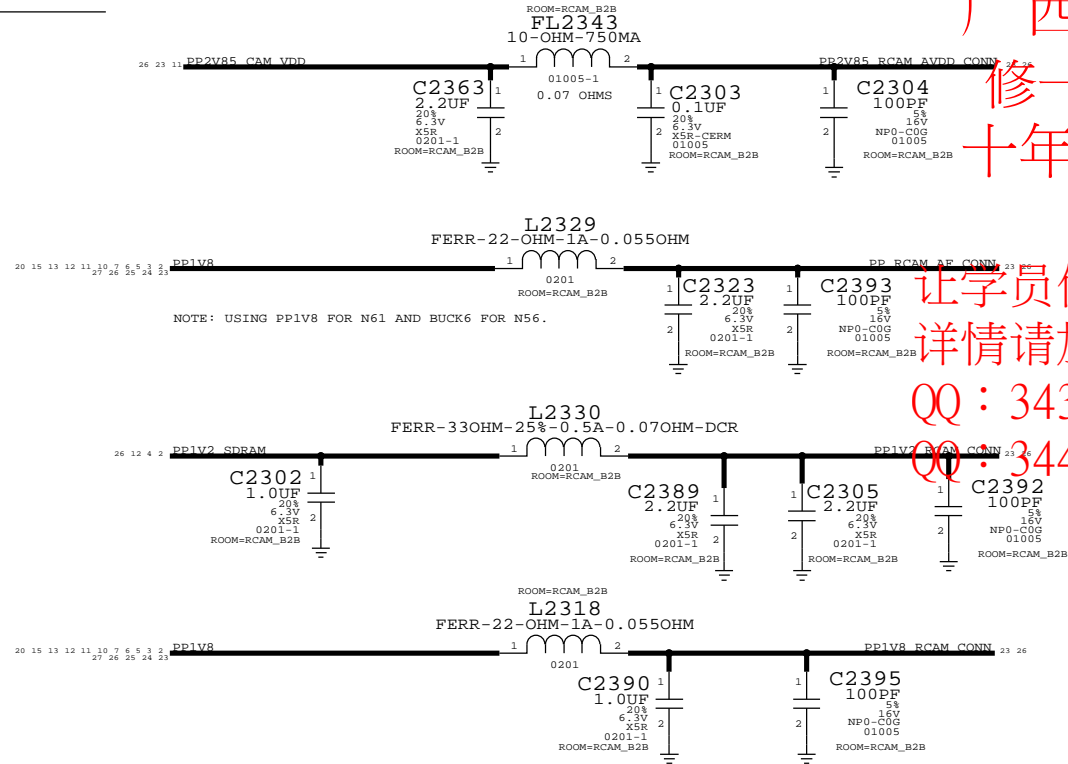
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4-LANE MIPI



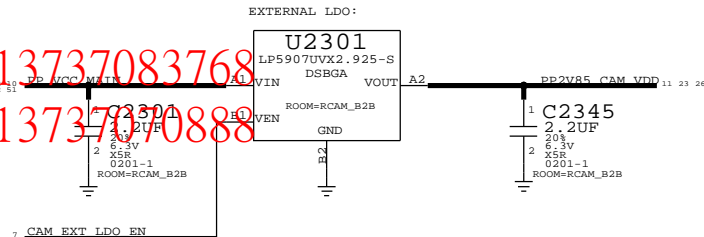
RCAM:  
DIGITAL I/F  
(I2C, CTRL, CLK)



RCAM:  
POWER:  
(1.8V DVDD)  
(2.8V AVDD)  
(1.2V VCC)  
(1.8V/2V AF)




RCAM: RCAM AVDD RAIL EXT. LDO:



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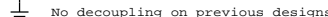
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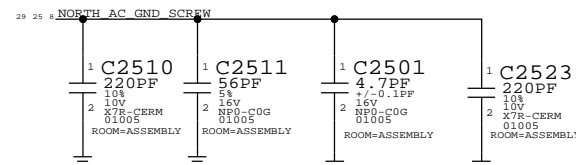
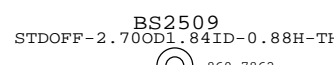
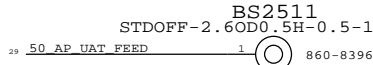
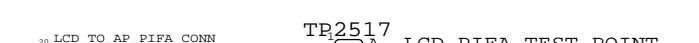
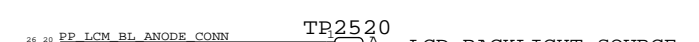
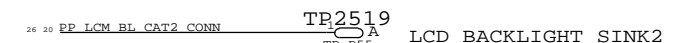
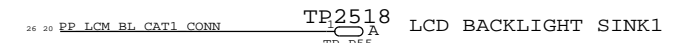
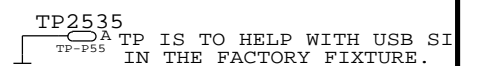
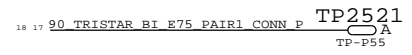
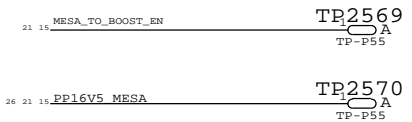
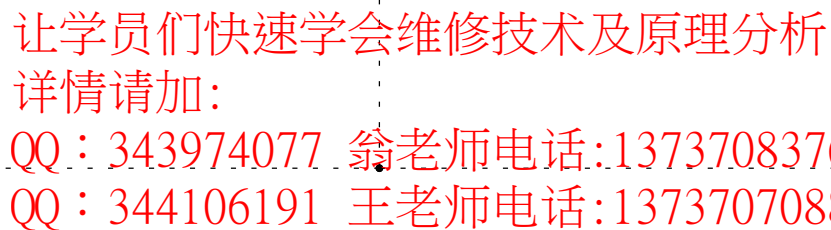
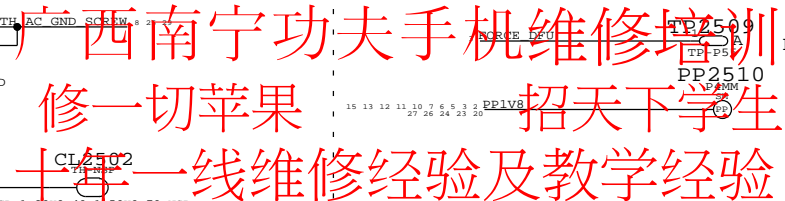


10

H

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8	7	6	5	4	3	2	1
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A


# VOLTAGE PROPERTIES

VOLTAGE=3.3V	PP3V3 USB	3 12	VOLTAGE=5.0V	PP LED DRV LX	14
VOLTAGE=1.8V	PP1V8 VA I19 I67	10 12 14 15 16 17 23 31 39	VOLTAGE=5.0V	PP LED BOOST OUT	16
VOLTAGE=3.0V	PP3V0 TRISTAR	12 15 17 29			
VOLTAGE=3.0V	PP3V0 IMU	12 19	VOLTAGE=2.9V	PP2V9 LDO9	12
VOLTAGE=3.0V	PP3V0 NAND	6 12			
VOLTAGE=3.0V	PP3V3 ACC	12 17	VOLTAGE=1.8V	PP CODEC TO MIC1 BIAS CONN	18
VOLTAGE=3.0V	PP3V0 PROX ALS	11 12	VOLTAGE=4.6V	PP E75 TO TRISTAR ACC2	17 18
			VOLTAGE=4.6V	PP E75 TO TRISTAR ACC2 CONN	18 25
VOLTAGE=4.6V	PP VCC MAIN	10 12 14 15 16 17 23 31 39	VOLTAGE=1.8V	PP1V8 LCM CONN	20
VOLTAGE=1.0V	PP1V0	7 12	VOLTAGE=22.0V	PP LCM BL ANODE CONN	20 25
VOLTAGE=3.0V	PP3V0 PROX TRIED	11 12	VOLTAGE=-5.7V	PP5V7 LCM AVDDN CONN	20
VOLTAGE=1.8V	PP1V8 ALWAYS	1 5 12 14	VOLTAGE=5.7V	PP5V7 LCM AVDDH CONN	20
VOLTAGE=3.0V	PP3V0 MESA	12 21			
VOLTAGE=1.1V	PP CPU	4 12			
VOLTAGE=1.1V	PP GPU	4 12	VOLTAGE=1.8V	PP1V8 MESA	21
			VOLTAGE=16.5V	PP16V5 MESA CONN	21
VOLTAGE=1.2V	PP1V2 SDRAM	2 4 12 23	VOLTAGE=5.0V	PP TRISTAR PIN	17
VOLTAGE=1.8V	PP1V8 SDRAM	3 4 10 12 13 14 15 17 29			
VOLTAGE=1.8V	PP1V8	2 4 5 7 10 11 12 13 15 20 23			
VOLTAGE=1.8V	PP1V8 GRAPE	12 24	VOLTAGE=1.2V	PP E75 TO TRISTAR ACC2 CONN	17 18
VOLTAGE=1.8V	PP1V8 OSCAR	12 19 22	VOLTAGE=1.8V	PP1V8 SCAM CONN	23
VOLTAGE=1.2V	PP1V2 NAND VDD	12 19 22	VOLTAGE=3.0V	PP2V95 LCM VDD	23
VOLTAGE=1.8V	PP EXTMIC BIAS FILT IN	16	VOLTAGE=1.8V	PP2V95 LCM AVDD CONN	23
VOLTAGE=1.8V	BOARD ID2	3 27	VOLTAGE=1.8V	PP CUMULUS VDDCORE	24
VOLTAGE=1.2V	PP1V2	2 4 5 12 23	VOLTAGE=1.8V	PP SAGE TO TOUCH VCPH	24
VOLTAGE=5.0V	PP E75 TO TRISTAR ACC2 CONN	17 18	VOLTAGE=-12V	PP SAGE TO TOUCH VCPH	24
VOLTAGE=5.0V	PP E75 TO TRISTAR ACC2	17 18	VOLTAGE=13.5V	PP SAGE TO TOUCH VCPH	24
VOLTAGE=22.0V	PP LCM BL ANODE	15 20	VOLTAGE=-12V	PP SAGE TO TOUCH VCPH	24
VOLTAGE=0.2V	PP LCM BL CAT1	15 20			
VOLTAGE=0.2V	PP LCM BL CAT1	15 20			
VOLTAGE=0.2V	PP LCM BL CAT1 CONN	20 25			
VOLTAGE=0.2V	PP LCM BL CAT1 CONN	20 25			
VOLTAGE=-5.7V	PP5V7 SAGE AVDDN	15 20 24	VOLTAGE=-12V	PP SAGE VCPH_F	24
VOLTAGE=1.2V	PP1V2 OSCAR	12 22	VOLTAGE=-12V	PP SAGE VCPH_F	24
VOLTAGE=3.0V	PP3V0 MESA SW	15			
VOLTAGE=6V	PP6V0 LCM BOSS	1 16			
VOLTAGE=5.0V	PP STRB_DRIVER TO LED WARM	1 16			
VOLTAGE=5.0V	PP STRB_DRIVER TO LED WARM	1 16			
			VOLTAGE=1.8V	PP PMU VREF	13
			VOLTAGE=14V	PP_SAGE_VBST_OUTH	24
VOLTAGE=1.8V	PP CODEC TO MIC1 BIAS	18	VOLTAGE=5.0V	PP1V8 VDD	13
VOLTAGE=1.8V	PP EXTMIC BIAS IN	16	VOLTAGE=5.0V	PP1V8 VDD	13
VOLTAGE=1.8V	PP EXTMIC BIAS IN	16	VOLTAGE=5.0V	PP1V8 VDD	13
VOLTAGE=1.8V	PP CODEC TO FRONTMIC2 BIAS	18	VOLTAGE=4.6V	PP BATT VCC	14 16 25 40 45 46
VOLTAGE=1.8V	PP CODEC TO FRONTMIC2 BIAS	18	VOLTAGE=1.8V	PP1V8 MESA CONN	21
VOLTAGE=1.8V	PP CODEC FILT	8 10	VOLTAGE=3.0V	PP3V0 PROX CONN	11
VOLTAGE=2.2V	PP CODEC SPKR VO	10			
VOLTAGE=2.5V	PP CODEC VCPREILT	10			
VOLTAGE=2.5V	PP CODEC VCPREILT	10			
VOLTAGE=2.5V	PP CODEC VHP ELVN	10			
VOLTAGE=0.2V	PP CODEC VHP ELVX	10			
VOLTAGE=2.5V	PP CODEC VHP ELVX	10			
VOLTAGE=1.8V	PP1V8 ECAM CONN	11			
VOLTAGE=3.0V	PP2V95 ECAM AVDD CONN	11			
VOLTAGE=1.8V	PP CODEC TO FRONTMIC1 BIAS CONN	11	VOLTAGE=1.0V	PP0V95 FIXED SOC	4 7 12
VOLTAGE=3.0V	PP3V0 ALS CONN	11	VOLTAGE=1.0V	PP0V95 FIXED SOC RCIE	7
VOLTAGE=1.2V	PP1V2 ECAM VDDIO CONN	11	VOLTAGE=1.2V	PP1V2 PLL	2
VOLTAGE=5.0V	PP5V0 USB	12 14 17 18 25	VOLTAGE=1.0V	PP BUCK5 LX	12
VOLTAGE=5.0V	PP5V0 USB TO PMU	12	VOLTAGE=1.0V	PP VAR SOC	5 12
VOLTAGE=4.6V	PP BUCK5 LX	12			
VOLTAGE=4.6V	PP BUCK3 LX	12			
VOLTAGE=4.6V	PP BUCK4 LX	12			
VOLTAGE=4.6V	PP BUCK2 LX	12	VOLTAGE=5.0V	PMID CAP	14
VOLTAGE=4.6V	PP BUCK1 LX	12	VOLTAGE=5.0V	CHARGER LDO	14
VOLTAGE=4.6V	PP BUCK1 LX	12	VOLTAGE=4.6V	CHG BOOT	14
VOLTAGE=4.6V	PP BUCK0 LX	12	VOLTAGE=4.6V	CHG LX	14
VOLTAGE=4.6V	PP BUCK0 LX	12	VOLTAGE=3.0V	VIBR DRIVE P	14 18
VOLTAGE=4.6V	PP BUCK0 LX	12	VOLTAGE=3.0V	VIBR DRIVE N	14 18
VOLTAGE=6.0V	PP CHESTNUT LXP	15			
VOLTAGE=6.0V	PP CHESTNUT CP	15	VOLTAGE=1.8V	PP RCAM AE CONN	23
VOLTAGE=6.0V	PP CHESTNUT CN	15	VOLTAGE=-14.0V	PP SAGE_VBST_OUTH	24
VOLTAGE=5.7V	PP5V7 SAGE AVDDH	16 24	VOLTAGE=-12.0V	PP SAGE TO TOUCH VCPH_FILT	24
VOLTAGE=5.7V	PP5V7 LCM AVDDH	15 20			
VOLTAGE=5.1V	PP5V1 GRAPE VDDH	15 24	VOLTAGE=2.7V	PP BB VDD 2V7 CONN	18
VOLTAGE=22.0V	PP WLED LX	15			
VOLTAGE=18.0V	PP18V0 MESA SW	15			
VOLTAGE=17.0V	PP17V0 MESA JDOIN	15			
VOLTAGE=16.5V	PP16V5 MESA	15 21 25			
VOLTAGE=8.0V	PP_SPKAMP_SW	16			
VOLTAGE=8.0V	PP I19 VBOOST	16			
VOLTAGE=1.8V	PP_SPKAMP_FILT	16			
VOLTAGE=1.8V	PP_SPKAMP LDO FILT	16			

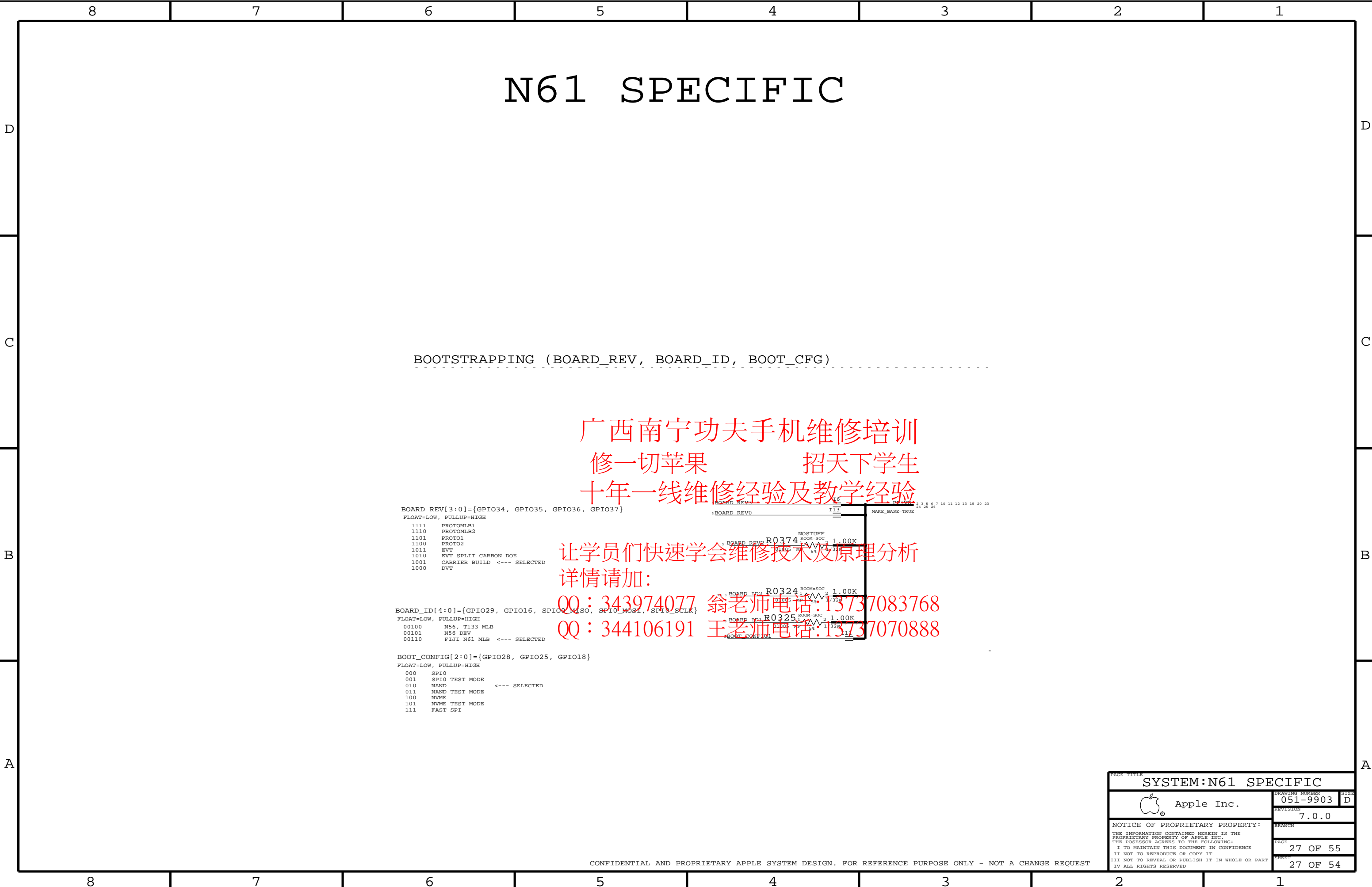
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# N61 SPECIFIC

BOOTSTRAPPING (BOARD\_REV, BOARD\_ID, BOOT\_CFG)

```
BOARD_REV[3:0]={GPIO34, GPIO35, GPIO36, GPIO37}
FLOAT=LOW, PULLUP=HIGH
1111 PROTOMLB1
1110 PROTOMLB2
1101 PROTO1
1100 PROTO2
1011 EVT
1010 EVT SPLIT CARBON DOE
1001 CARRIER BUILD <--- SELECTED
1000 DVT
```

```
BOARD_ID[4:0]={GPIO29, GPIO16, SPI0_MISO, SPI0_MOSI, SPI0_SCLK}
FLOAT=LOW, PULLUP=HIGH
00100 N56, T133 MLB
00101 N56 DEV
00110 FIJI N61 MLB <--- SELECTED
```


```
BOOT_CONFIG[2:0]={GPIO28, GPIO25, GPIO18}
FLOAT=LOW, PULLUP=HIGH
000 SPI0
001 SPI0 TEST MODE
010 NAND <--- SELECTED
011 NAND TEST MODE
100 NVME
101 NVME TEST MODE
111 FAST SPI
```

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8	7	6	5	4	3	2	1
D							D
C							C
B							B
A							A
8	7	6	5	4	3	2	1

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# RADIO\_MLB HIERARCHICAL SYMBOL

## POWER

VCC\_MAIN, VBAT GOES TO RADIO\_MLB DIRECTLY  
CHECK ALL PAGES IN RF SIDE!

## CELLULAR HOUSE KEEPING

3	AP_TO_RADIO_ON_L	MAKE_BASE+TRUE	I325	RADIO_ON_L	30 32
3	BB_TO_AP_RESET_DET_L	MAKE_BASE+TRUE	I324	BB_RESET_DET_L	30 32
3	PMU_TO_BB_RST_L	MAKE_BASE+TRUE	I326	RF_PMIC_RESET_L	30 32
3	AP_TO_BB_RST_L	MAKE_BASE+TRUE	I327	BB_RST_L	30 32
3	AP_TO_BB_WAKE_MODEM	MAKE_BASE+TRUE	I329	AP_WAKE_MODEM	35
3	BB_TO_PMU_HOST_WAKE_L	MAKE_BASE+TRUE	I328	BB_WAKE_HOST_L	30 35
13	BB_TO_AP_IPC_GPIO	MAKE_BASE+TRUE	I331	BB_IPC_GPIO	35
16	BB_TO_LEDDR_V_GSM_BLANK	MAKE_BASE+TRUE	I330	GSM_TXBURST_IND	35
3	BB_TO_AP_GPS_SYNC	MAKE_BASE+TRUE	I332	BB_GPS_SYNC	30 35

## HSIC IPC

2	50_AP_BI_BB_HSIC1_DATA	MAKE_BASE+TRUE	I368	50_BB_HSIC_DATA	30 34
2	50_AP_BI_BB_HSIC1_STB	MAKE_BASE+TRUE	I369	50_BB_HSIC_STROBE	30 34
3	AP_TO_BB_HOST_RDY	MAKE_BASE+TRUE	I371	BB_HOST_RDY	30 35
3	BB_TO_AP_DEVICE_RDY	MAKE_BASE+TRUE	I370	BB_DEVICE_RDY	30 35
3	BB_TO_AP_IPC_GPIO1	MAKE_BASE+TRUE	I372	BB_IPC_GPIO1	30 35

## UART IPC

3	AP_TO_BB_UART2_RTS_L	MAKE_BASE+TRUE	I373	BB_UART2_RTS_L	30 35
3	BB_TO_AP_UART2_CTS_L	MAKE_BASE+TRUE	I374	BB_UART2_CTS_L	30 35
17 3	AP_TO_BB_UART2_TXD	MAKE_BASE+TRUE	I375	BB_UART2_TXD	30 35
17 3	BB_TO_AP_UART2_RXD	MAKE_BASE+TRUE	I376	BB_UART2_RXD	30 35

## AUDIO I2S

3	45_AP_TO_BB_I2S3_BCLK	MAKE_BASE+TRUE	I377	BB_I2S_CLK	35
3	AP_TO_BB_I2S3_DOUT	MAKE_BASE+TRUE	I378	BB_I2S_RXD	30 35
3	BB_TO_AP_I2S3_DIN	MAKE_BASE+TRUE	I379	BB_I2S_TXD	30 35
3	AP_TO_BB_I2S3_LRCLK	MAKE_BASE+TRUE	I380	BB_I2S_WS	30 35

## OSCAR UART

22	OSCAR_TO_BB_UART_TXD	MAKE_BASE+TRUE	I382	BB_OTHER_UART_TXD	30 35
22	BB_TO_OSCAR_UART_RXD	MAKE_BASE+TRUE	I381	BB_OTHER_UART_RXD	30 35

## BB DEBUG INTERFACES

3	AP_TO_BB_COREDUMP	MAKE_BASE+TRUE	I384	BB_CORE_DUMP	30 35
13	PMU_TO_BB_VBUS_DET	MAKE_BASE+TRUE	I387	BB_USB_VBUS	30 34
17	90_TRISTAR_BI_BB_USB_N	MAKE_BASE+TRUE	I385	90_BB_USB_N	30 34
17	90_TRISTAR_BI_BB_USB_P	MAKE_BASE+TRUE	I386	90_BB_USB_P	30 34

## RADIO ANTENNA CONTROL

18	PP_BB_VDD_2V7	MAKE_BASE+TRUE	I389	PP_LDO14_RFSW	31 41 42
18	BB_GPIO0	MAKE_BASE+TRUE	I390	BB_LAT_GPIO0	35
18	BB_GPIO2	MAKE_BASE+TRUE	I391	BB_LAT_GPIO2	35
18	BB_GPIO3	MAKE_BASE+TRUE	I392	BB_LAT_GPIO3	35
18	BB_GPIO4	MAKE_BASE+TRUE	I394	BB_LAT_GPIO4	35

## FCT TESTING

13	RADIO_TO_PMU_ADC_SMPS1	MAKE_BASE+TRUE	I395	ADC_SMPS1	30
13	RADIO_TO_PMU_ADC_PP_LDO11_VDDIO	MAKE_BASE+TRUE	I396	ADC_PP_LDO11	30
13	RADIO_TO_PMU_ADC_PP_LDO5_SIM	MAKE_BASE+TRUE	I398	ADC_PP_LDO5	30
13	RADIO_TO_PMU_ADC_SMPS4	MAKE_BASE+TRUE	I397	ADC_SMPS4	30

## UPPER RADIO ANTENNA CONTROL

25	50_AP_WIFI_5G_CONN_ANT	MAKE_BASE+TRUE	I410	50_WIFI_5G_CONN_ANT	50
25	50_AP_UAT_FEED	MAKE_BASE+TRUE	I409	50_UPPER_ANT_FEED	50
	UAT_ANT_GND	MAKE_BASE+TRUE	I411	ANT_GND	50
29 26 17 15 12	PP3V0_TRISTAR	MAKE_BASE+TRUE	I404	PAC_VDD_3V0	53
25 8	NORTH_AC_GND_SCREW	MAKE_BASE+TRUE	I412	NORTH_ANT_GND	50

## POWER

26 17 15 14 13 12 10 4 3	PP1V8_SDRAM	MAKE_BASE+TRUE	I314	PP_WL_BT_VDDIO_AP	51
		MAKE_BASE+TRUE	I315	PP_STOCKHOLM_IVR_S2P	52 54
		MAKE_BASE+TRUE	I407	REFE_VIO_S2P	53

## WLAN/BT HOUSE KEEPING

13	45_PMU_TO_WLAN_CLK32K	MAKE_BASE+TRUE	I316	CLK32K_AP	30 51
13	PMU_TO_WLAN_REG_ON	MAKE_BASE+TRUE	I317	WLAN_REG_ON	30 51
13	WLAN_TO_PMU_HOST_WAKE	MAKE_BASE+TRUE	I318	HOST_WAKE_WLAN	30 51
13	PMU_TO_BT_REG_ON	MAKE_BASE+TRUE	I319	BT_REG_ON	30 51
3	AP_TO_BT_WAKE	MAKE_BASE+TRUE	I320	WAKE_BT	30 51
13	BT_TO_PMU_HOST_WAKE	MAKE_BASE+TRUE	I321	HOST_WAKE_BT	51

3	AP_TO_WLAN_JTAG_SWCLK	MAKE_BASE+TRUE	I333	WLAN_JTAG_SWCLK	30 51
3	AP_TO_WLAN_JTAG_SWDIO	MAKE_BASE+TRUE	I334	WLAN_JTAG_SWDIO	30 51
13	WLAN_TO_PMU_PCIE_WAKE_L	MAKE_BASE+TRUE	I335	WLAN_PCIE_WAKE_L	30 51
3	AP_TO_WLAN_DEVICE_WAKE	MAKE_BASE+TRUE	I336	PCIE_DEV_WAKE	30 51
3	90_WLAN_TO_AP_PCIE1_TDP_P	MAKE_BASE+TRUE	I337	90_WLAN_PCIE_TDP	30 51
3	90_WLAN_TO_AP_PCIE1_TDN_N	MAKE_BASE+TRUE	I338	90_WLAN_PCIE_TDN	30 51
3	90_WLAN_TO_AP_PCIE1_RDP_P	MAKE_BASE+TRUE	I339	90_WLAN_PCIE_RDP	30 51
3	90_WLAN_TO_AP_PCIE1_RDN_N	MAKE_BASE+TRUE	I340	90_WLAN_PCIE_RDN	30 51
3	90_WLAN_TO_AP_PCIE1_REFCCLK_P	MAKE_BASE+TRUE	I341	90_WLAN_PCIE_REFCCLK_P	51
3	90_WLAN_TO_AP_PCIE1_REFCCLK_N	MAKE_BASE+TRUE	I342	90_WLAN_PCIE_REFCCLK_N	51
3	WLAN_TO_AP_PCIE1_CLKREQ_L	MAKE_BASE+TRUE	I343	WLAN_PCIE_CLKREQ_L	30 51
3	AP_TO_WLAN_PCIE1_RST_L	MAKE_BASE+TRUE	I344	WLAN_PCIE_PERST_L	30 51

## WLAN HSIC IPC

3	WLAN_TO_AP_UART4_RXD	MAKE_BASE+TRUE	I345	WLAN_UART_TXD	30 51
3	AP_TO_WLAN_UART4_TXD	MAKE_BASE+TRUE	I346	WLAN_UART_RXD	30 51
3	WLAN_TO_AP_UART4_CTS_L	MAKE_BASE+TRUE	I347	WLAN_UART_RTS_L	30 51
3	AP_TO_WLAN_UART4_RTS_L	MAKE_BASE+TRUE	I348	WLAN_UART_CTS_L	30 51

## BT UART IPC

3	AP_TO_BT_UART1_RTS_L	MAKE_BASE+TRUE	I349	BT_UART_CTS_L	51
3	BT_TO_AP_UART1_CTS_L	MAKE_BASE+TRUE	I350	BT_UART_RTS_L	51
3	AP_TO_BT_UART1_TXD	MAKE_BASE+TRUE	I351	BT_UART_RXD	30 51
3	BT_TO_AP_UART1_RXD	MAKE_BASE+TRUE	I352	BT_UART_TXD	30 51

## BT AUDIO PCM

3	45_AP_TO_BT_I2S1_BCLK	MAKE_BASE+TRUE	I354	BT_PCM_CLK	51
3	AP_TO_BT_I2S1_DOUT	MAKE_BASE+TRUE	I355	BT_PCM_IN	51
3	BT_TO_AP_I2S1_DIN	MAKE_BASE+TRUE	I356	BT_PCM_OUT	51
3	AP_TO_BT_I2S1_LRCLK	MAKE_BASE+TRUE	I357	BT_PCM_SYNC	51

## OSCAR STATES


22	OSCAR_TO_RADIO_CONTEXT_A	MAKE_BASE+TRUE	I358	OSCAR_CONTEXT_A	51
22	OSCAR_TO_RADIO_CONTEXT_B	MAKE_BASE+TRUE	I359	OSCAR_CONTEXT_B	51

## STOCKHOLM

3	STOCKHOLM_TO_AP_UART3_CTS_L	MAKE_BASE+TRUE	I359	STOCKHOLM_RTS_L	30 52
3	AP_TO_STOCKHOLM_UART3_RTS_L	MAKE_BASE+TRUE	I360	STOCKHOLM_CTS_L	30 52
3	STOCKHOLM_TO_AP_UART3_RXD	MAKE_BASE+TRUE	I361	STOCKHOLM_UART_TXD	30 52
3	AP_TO_STOCKHOLM_UART3_TXD	MAKE_BASE+TRUE	I362	STOCKHOLM_UART_RXD	30 52
3	AP_TO_STOCKHOLM_DWLD_REQ	MAKE_BASE+TRUE	I363	STOCKHOLM_FW_DWLD_REQ	52
13	STOCKHOLM_TO_PMU_HOST_WAKE	MAKE_BASE+TRUE	I364	STOCKHOLM_HOST_WAKE	30 52
3	AP_TO_STOCKHOLM_EN	MAKE_BASE+TRUE	I365	STOCKHOLM_ENABLE	52
29 26 17 15 12	PP3V0_TRISTAR	MAKE_BASE+TRUE	I366	STOCKHOLM_VDD_MUX_3V0	54
3	AP_TO_STOCKHOLM_SIM_SEL	MAKE_BASE+TRUE	I367	STOCKHOLM_SIM_SEL	54
25	AP_TO_STOCKHOLM_ANT	MAKE_BASE+TRUE	I406	STOCKHOLM_ANT	52

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# AP INTERFACE & DEBUG CONNECTORS

## PROBE POINTS

PP3105_RF	P2MM-SM	1	CLK32K_AP	29 51
PP3113_RF	P4MM-SM	1	BB_COEX_UART_RXD	35 51
PP3114_RF	P4MM-SM	1	BB_COEX_UART_TXD	35 51
PP3119_RF	P2MM-SM	1	BT_UART_TXD	29 51
PP3120_RF	P2MM-SM	1	BT_UART_RXD	29 51
PP3152_RF	P2MM-SM	1	WAKE_BT	29 51
PP3153_RF	P2MM-SM	1	WLAN_REG_ON	29 51
PP3154_RF	P4MM-SM	1	BT_REG_ON	29 51
PP3155_RF	P2MM-SM	1	HOST_WAKE_WLAN	29 51
PP3156_RF	P2MM-SM	1	WLAN_PCIE_WAKE_L	29 51
PP3157_RF	P2MM-SM	1	WLAN_PCIE_PERST_L	29 51
PP3158_RF	P2MM-SM	1	WLAN_PCIE_CLKREQ_L	29 51
PP3159_RF	P4MM-SM	1	PCIE_DEV_WAKE	29 51
PP3160_RF	P2MM-SM	1	WLAN_UART_RTS_L	29 51
PP3161_RF	P2MM-SM	1	WLAN_UART_CTS_L	29 51
PP3162_RF	P2MM-SM	1	WLAN_UART_RXD	29 51
PP3163_RF	P2MM-SM	1	WLAN_UART_TXD	29 51
PP3190_RF	P2MM-SM	1	WLAN_JTAG_SWDCLK	29 51
PP3191_RF	P2MM-SM	1	WLAN_JTAG_SWDIO	29 51

PP3121_RF	P2MM-SM	1	STOCKHOLM_HOST_WAKE	29 52
PP3122_RF	P4MM-SM	1	BB_REQUEST_XO_CLK	32 52
PP3123_RF	P2MM-SM	1	STOCKHOLM_UART_RXD	29 52
PP3124_RF	P2MM-SM	1	STOCKHOLM_UART_TXD	29 52
PP3125_RF	P2MM-SM	1	STOCKHOLM_CTS_L	29 52
PP3126_RF	P2MM-SM	1	STOCKHOLM_RTS_L	29 52
PP3128_RF	P2MM-SM	1	PP_PN65_VCC_SIM	52
PP3144_RF	P4MM-SM	1	STOCKHOLM_SIM_SWP	52 54
PP3129_RF	P4MM-SM	1	REF_CLK_FROM_BB	32 52
PP3165_RF	P4MM-SM	1	DSDS_SIM_CLK	34 54
PP3183_RF	P4MM-SM	1	DSDS_SIM_RESET	34 54
PP3184_RF	P4MM-SM	1	DSDS_SIM_DATA	34 54
PP3185_RF	P4MM-SM	1	DSDS_SIM_DETECT	34
PP3187_RF	P4MM-SM	1	PP_LDO6	31 33 54
PP3188_RF	P4MM-SM	1	DSDS_SIM_SWP	54
PP3189_RF	P4MM-SM	1	DSDS_SIM_DATA_R	54
PP 3178_RF	P2MM-SM	1	BB_SIM_RESET	30 35
PP 3179_RF	P2MM-SM	1	BB_SIM_CLK	30 35
PP 3180_RF	P2MM-SM	1	BB_SIM_DATA	30 35
PP 3183_RF	P2MM-SM	1	BB_SIM_DETECT	30 35
PP 3184_RF	P2MM-SM	1	PP_LDO5	30 31 33 54

PP3115_RF	P4MM-SM	1	50_BB_HSIC_STROBE	29 34
PP3116_RF	P4MM-SM	1	50_BB_HSIC_DATA	29 34
PP3101_RF	P4MM-SM	1	BB_DEBUG_ERROR	35
PP3102_RF	P4MM-SM	1	RF_PMIC_RESET_L	29 32
PP3103_RF	P4MM-SM	1	PS_HOLD_PMIC	32
PP3127_RF	P4MM-SM	1	PMIC_RESOUT_L	32 34
PP3104_RF	P4MM-SM	1	MDM_CLK	32 34
PP3109_RF	P4MM-SM	1	PP_LDO11	30 31 33 35 38
PP3110_RF	P4MM-SM	1	RADIO_ON_L	29 32
PP3111_RF	P4MM-SM	1	SPMI_DATA	32 34
PP3112_RF	P4MM-SM	1	SPMI_CLK	32 34

PP3130_RF	P4MM-SM	1	BB_JTAG_RST_L	34
PP3131_RF	P4MM-SM	1	BB_JTAG_TCK	34
PP3132_RF	P4MM-SM	1	BB_JTAG_TMS	34
PP3133_RF	P4MM-SM	1	BB_JTAG_TDO	34
PP3134_RF	P4MM-SM	1	BB_JTAG_TDI	34
PP3135_RF	P4MM-SM	1	BB_JTAG_TEST_L	34
PP3137_RF	P4MM-SM	1	BB_PORC_CLAMP	29 35
PP3138_RF	P4MM-SM	1	BB_DEBUG_STATUS	35
PP3139_RF	P4MM-SM	1	90_BB_USB_N	29 34
PP3140_RF	P4MM-SM	1	90_BB_USB_P	29 34

PP3141_RF	P4MM-SM	1	BB_UART_TXD	29 35
PP3142_RF	P4MM-SM	1	BB_UART_RXD	29 35
PP3143_RF	P4MM-SM	1	BB_UART_RTS_L	29 35
PP3144_RF	P4MM-SM	1	BB_UART_CTS_L	29 35
PP3145_RF	P4MM-SM	1	BB_HOST_RDY	29 35
PP3146_RF	P4MM-SM	1	BB_DEVICE_RDY	29 35
PP3148_RF	P4MM-SM	1	BB_WAKE_HOST_L	29 35
PP3149_RF	P4MM-SM	1	BB_RESET_TTL_L	29 35
PP3150_RF	P4MM-SM	1	BB_RST_L	29 32
PP3151_RF	P4MM-SM	1	BOOT_HSIC	30 35

PP3170_RF	P4MM-SM	1	RFFE1_CLK	35 39 40 41 42 43 44
PP3171_RF	P4MM-SM	1	RFFE1_DATA	35 39 40 41 42 43 44
PP3172_RF	P4MM-SM	1	RFFE2_CLK	35 45 46 48
PP3173_RF	P4MM-SM	1	RFFE2_DATA	35 45 46 48
PP3175_RF	P4MM-SM	1	BB_I2S_WS	29 35
PP3176_RF	P4MM-SM	1	BB_I2S_RXD	29 35
PP3177_RF	P4MM-SM	1	BB_I2S_TXD	29 35
PP3178_RF	P4MM-SM	1	BB_OTHER_TXD	29 35
PP3179_RF	P4MM-SM	1	BB_OTHER_RXD	29 35

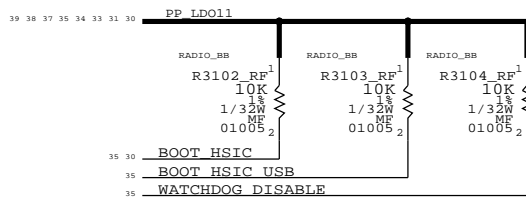
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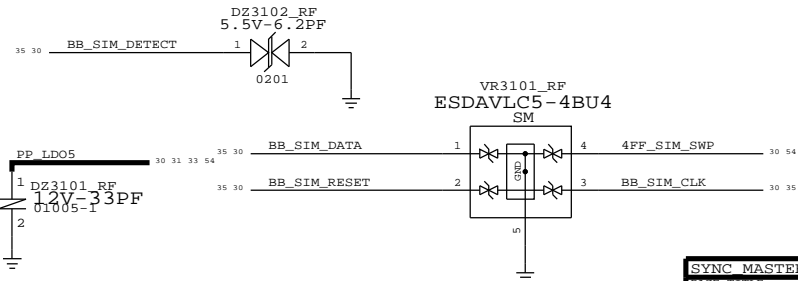
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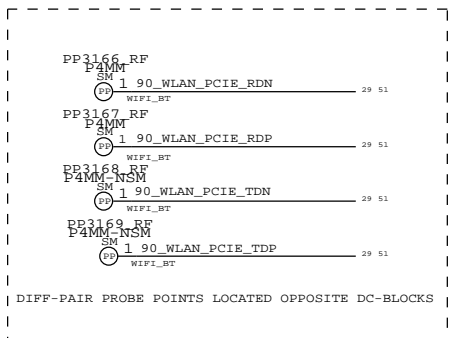
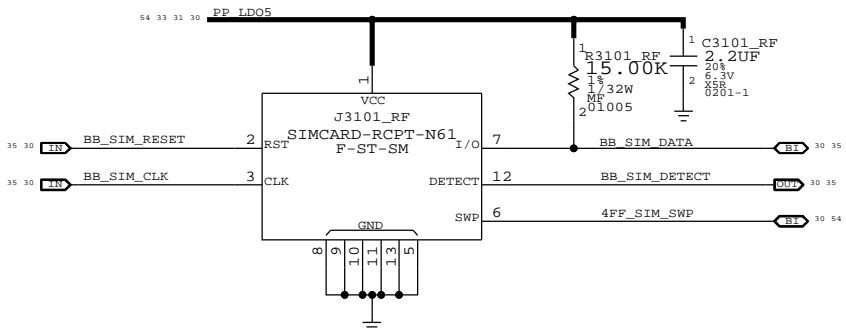
PART NUMBER	ALTERNATE PART NUMBER	ROW OPTION	REF. DES.	COMMENTS
197S0565	197S0593	ALTERNATE	Y3301_RF	KDS 19.2MHz XTAL
197S0598	197S0593	ALTERNATE	Y6301_RF	AVX19.2MHz XTAL
138S00005	138S00000	ALTERNATE	C3216_RF	150F CAPACITOR
138S0739	138S0706	ALTERNATE	C4207_RF	1.00UF CAPACITOR
138S0945	138S0706	ALTERNATE	C4207_RF	1.0UF CAPACITOR
138S1103	138S0719	ALTERNATE	C4007_RF	4.7UF CAPACITOR
339S0231	339S0228	ALTERNATE	U5201_RF	CORONA MODULE USI
339S0242	339S0228	ALTERNATE	U5201_RF	CORONA MODULE TDK
155S00024	155S0950	ALTERNATE	F_TRI_RF	TRIPLEXER BIN2



## SIM CARD ESD PROTECTION



## SIM CARD CONNECTOR

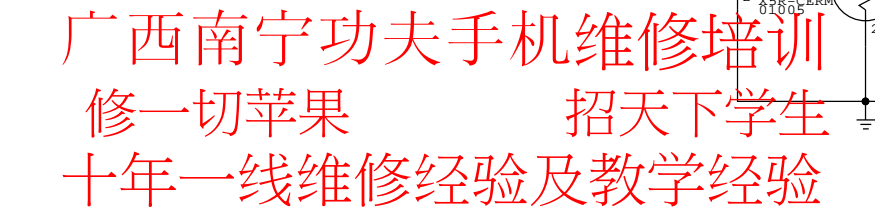


SYNC MASTER=N/A		SYNC DATE=N/A	
PAGE TITLE		AP INTERFACE & DEBUG CONNECTORS	
DRAWING NUMBER		051-9903	SIZE D
REVISION		7.0.0	
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SHEET		30 OF 54	
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BOARD_ID	REVISION
0.00V	N61 PROTO_MLB1
0.50V	N61 DEV3
0.70V	N61 DEV4
0.90V	N61 PROTO_MLB2
1.10V	N61/N56 PROTO1
1.30V	N61/N56 PROTO2
1.40V	N61/N56 EVT1
1.50V	N61/N56 EVT2 (CARRIER)
1.60V	N61/N56 DVT
1.70V	N61/N56 PVT

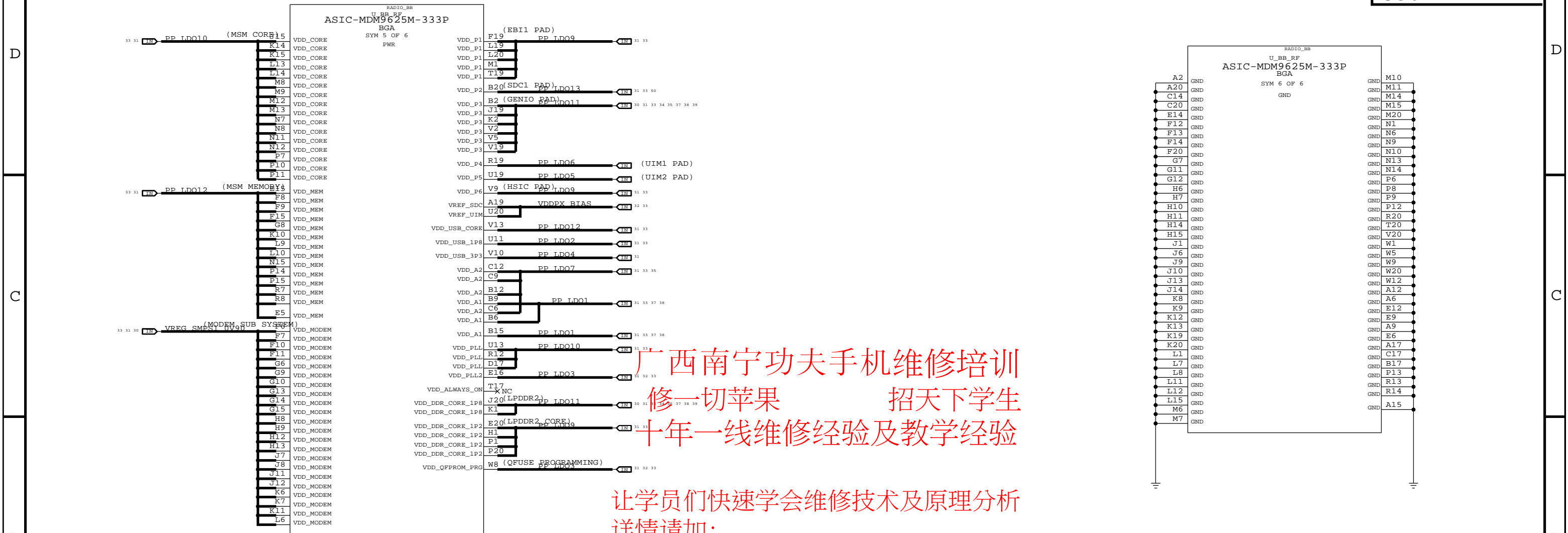


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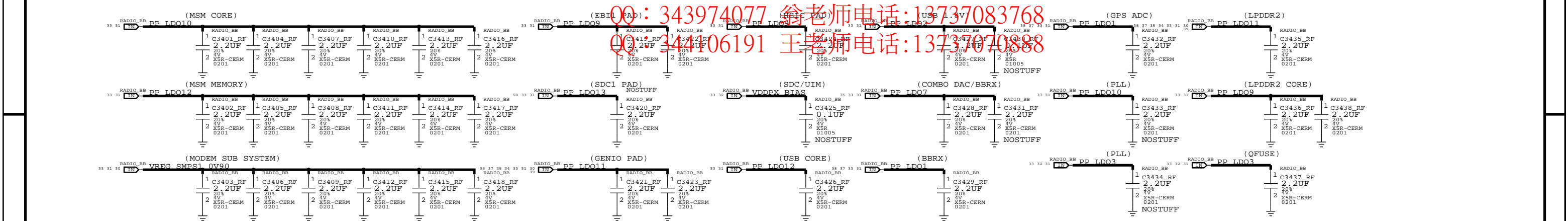
C538
R500
L500
U502



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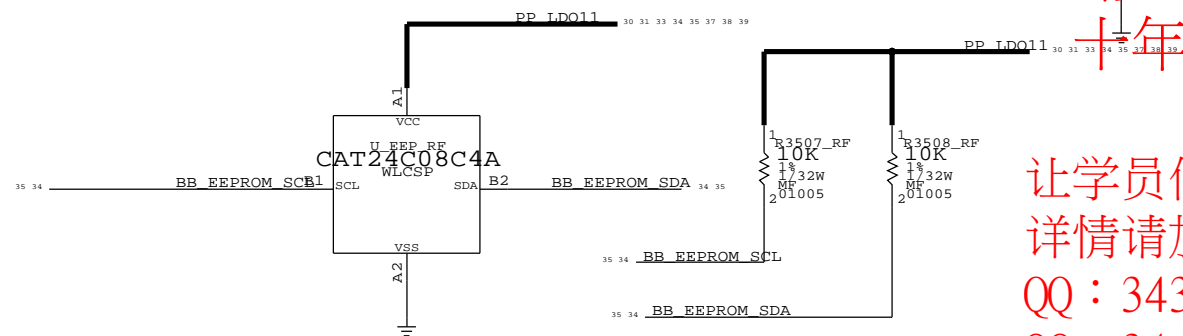
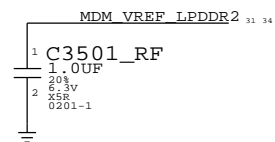
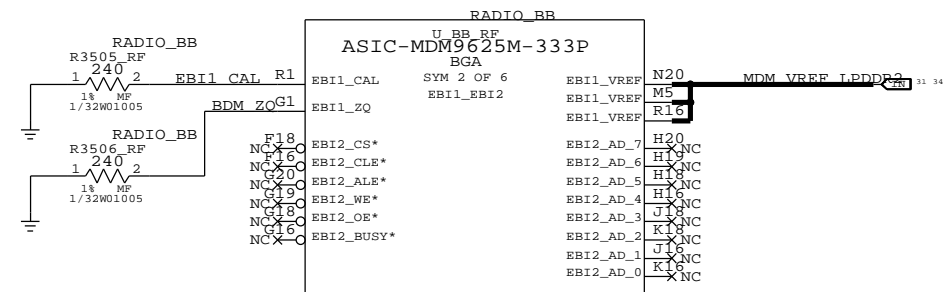
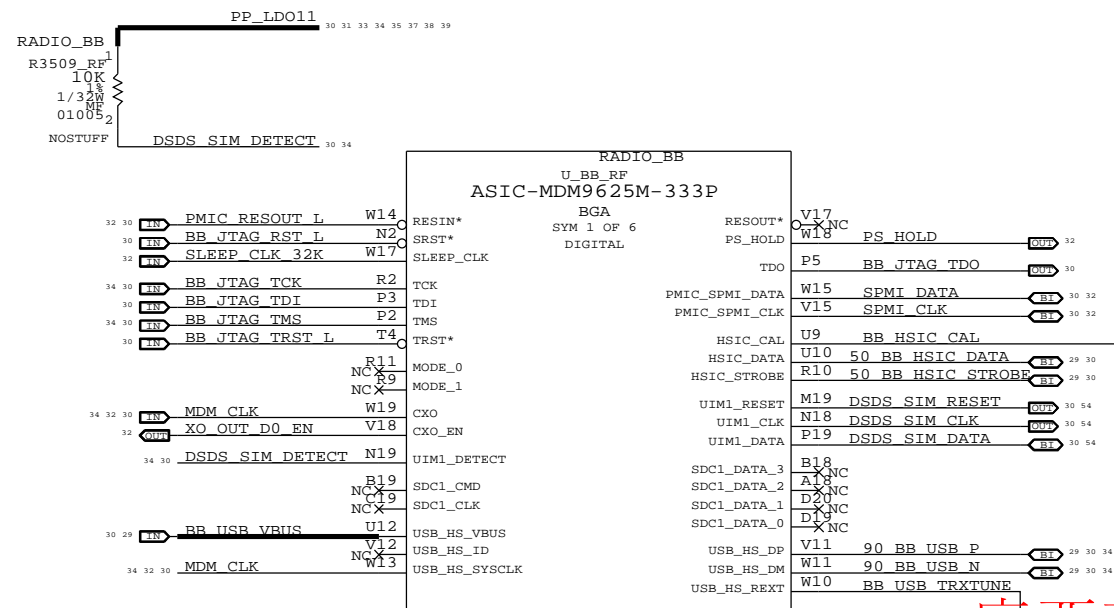
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C600
R606
L600
U602



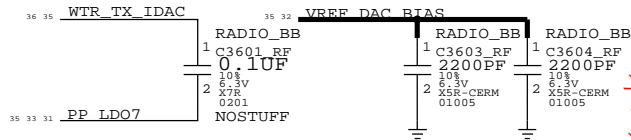
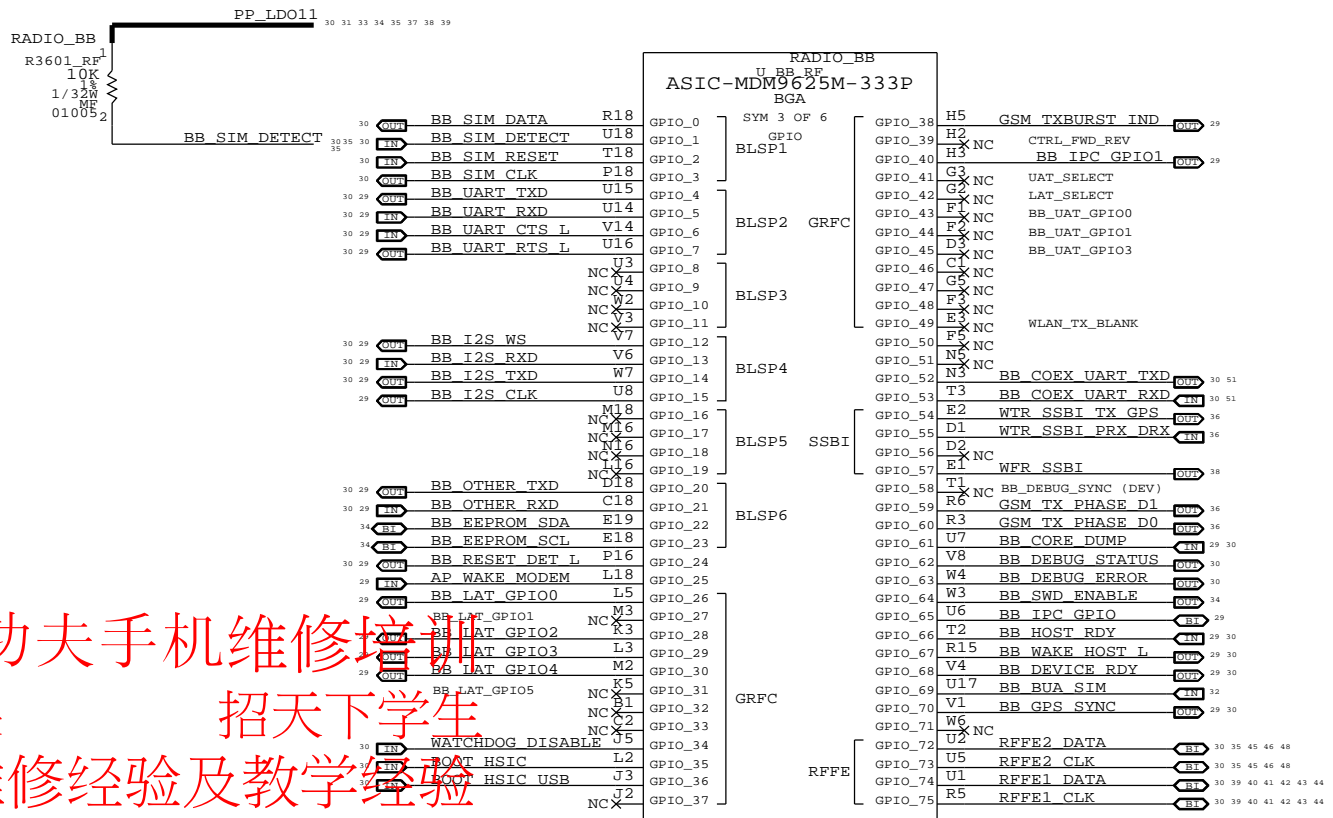
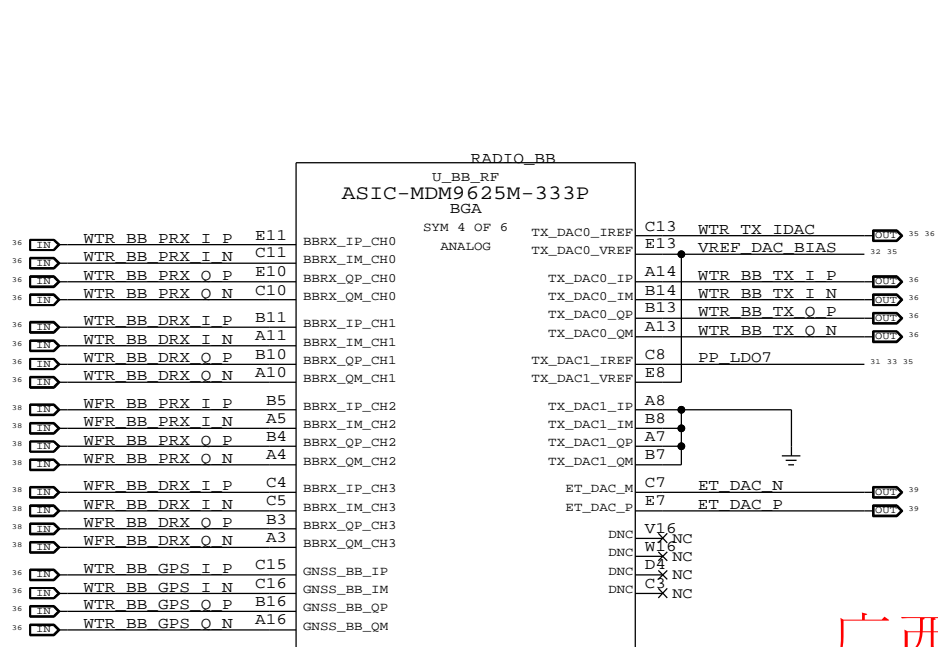
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C704
R700
L700
U702

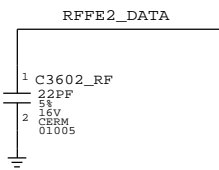
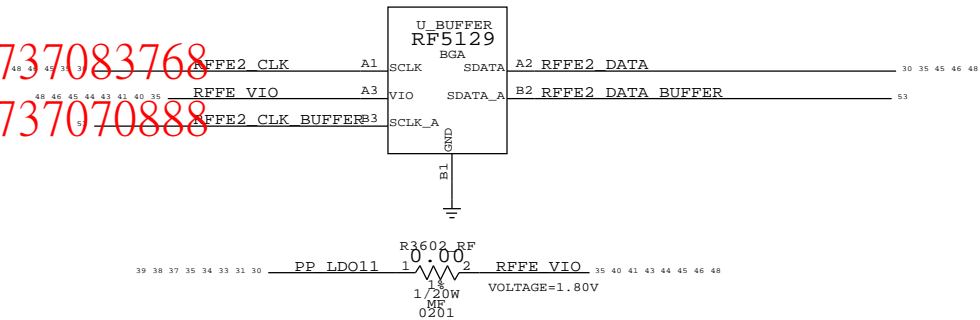


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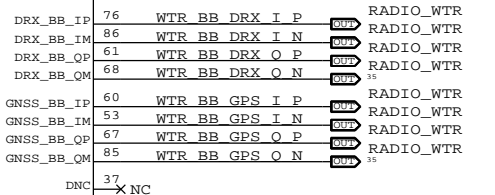
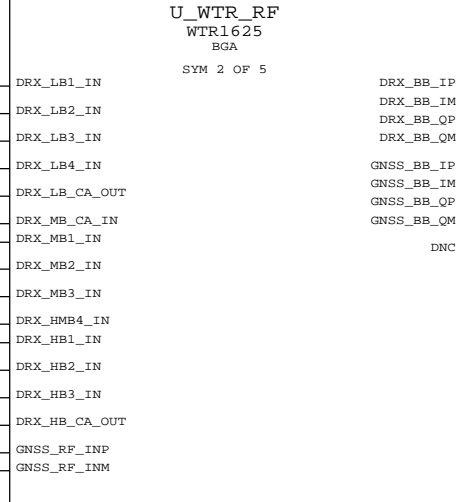
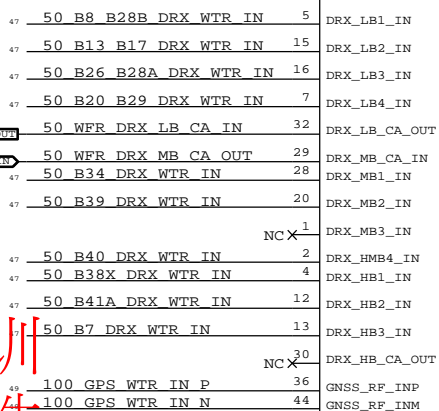
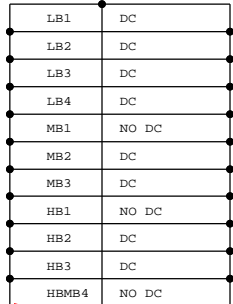
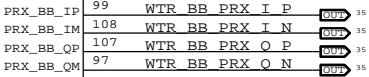
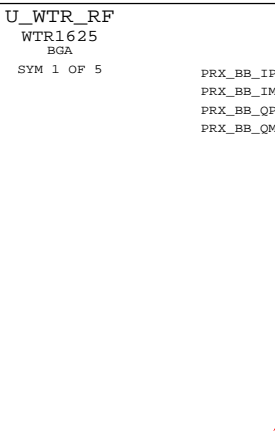
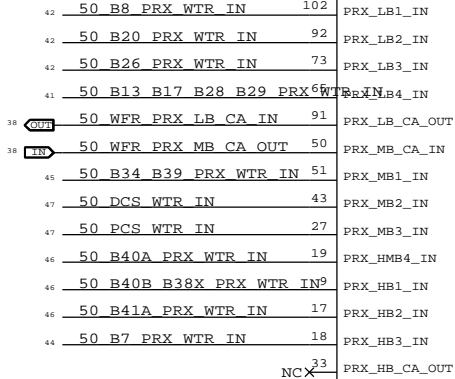
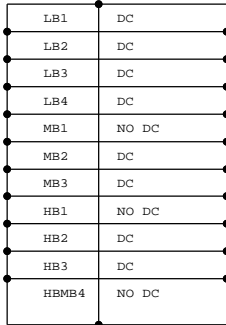
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# WTR TRANSCEIVER ( 1 OF 2 )

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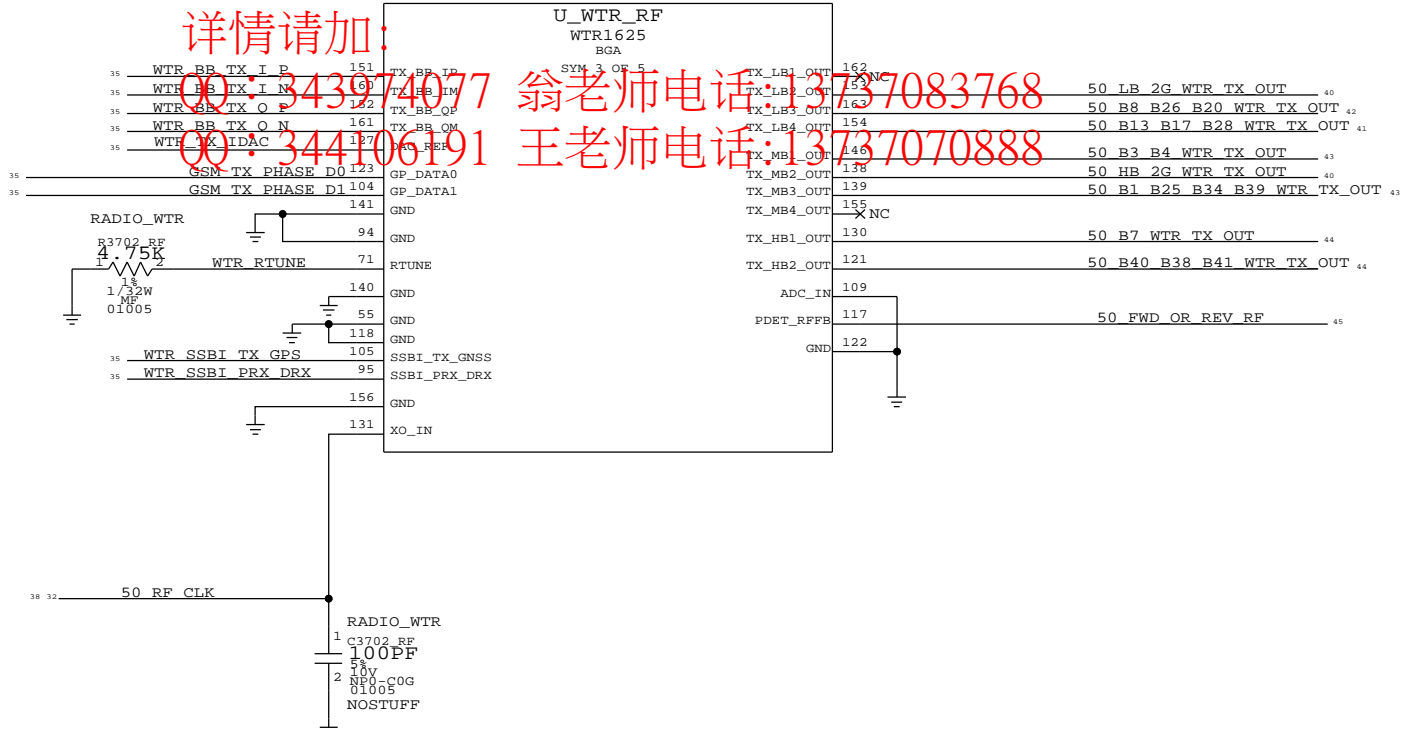
C802
R802
L800
U803



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RF\_CLK IS SHARED BETWEEN WTR AND WFR. LENGTH DIFFERENCE BETWEEN THE TWO SHOULD BE < 5MM.

RF TRANSCEIVER (1 OF 3)



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REVISION	7.0.0		

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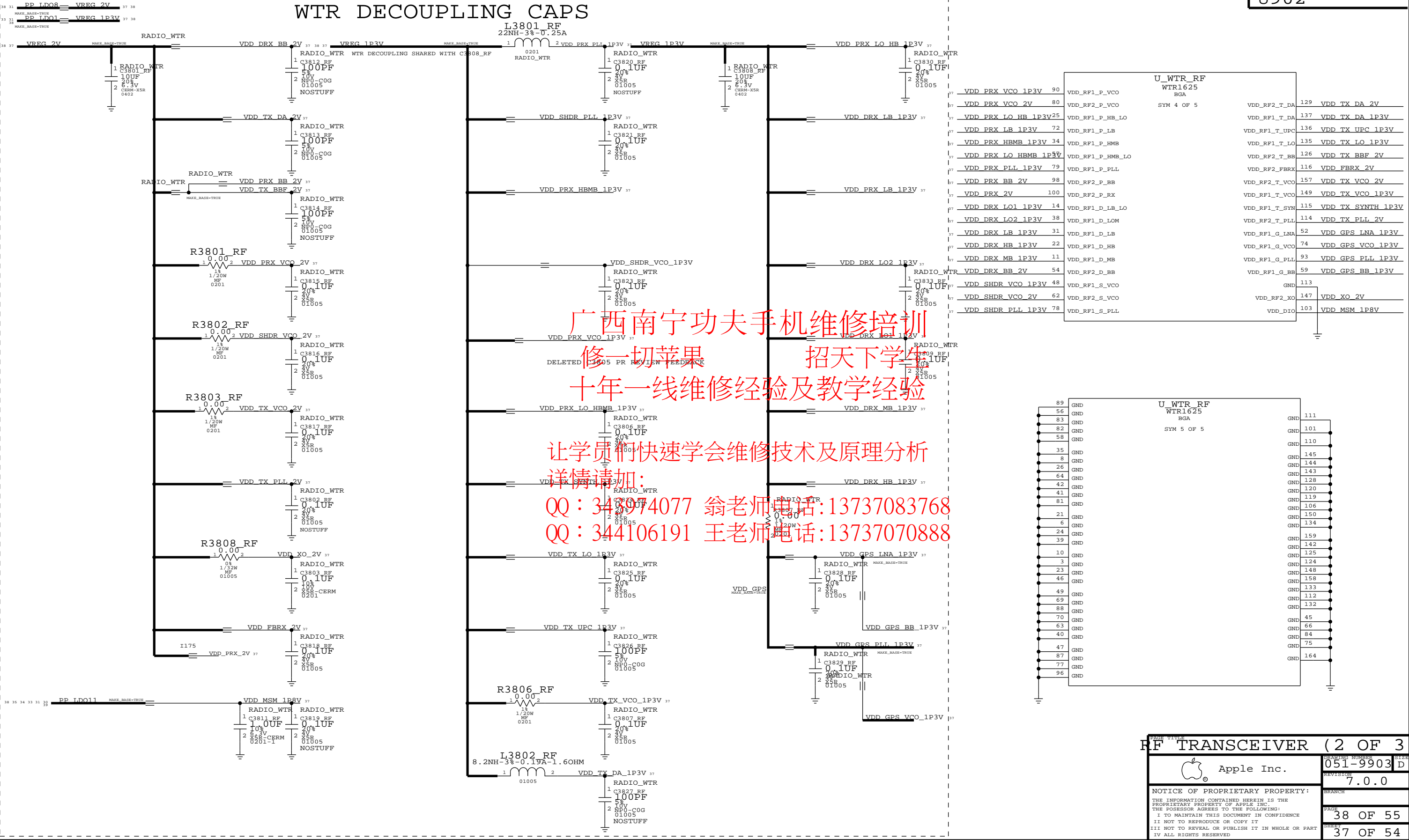
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C934  
R926  
L3802\_RF  
U902

## WTR DECOUPLING CAPS

L3801\_RF  
22NH-3%-0.25A



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RF TRANSCEIVER ( 2 OF 3 )

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051-9903 D

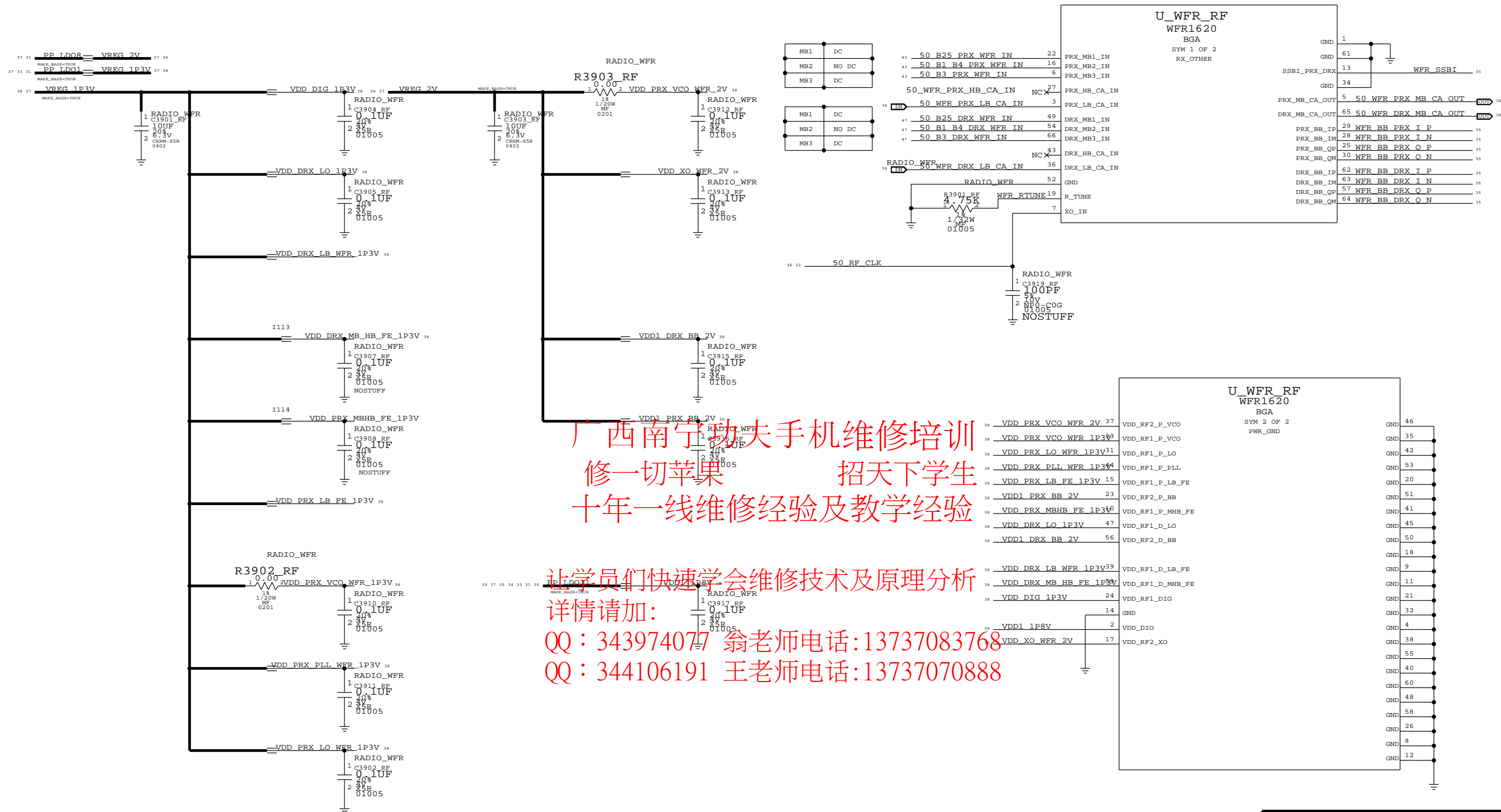
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C1019
R1016
L1000
U1002



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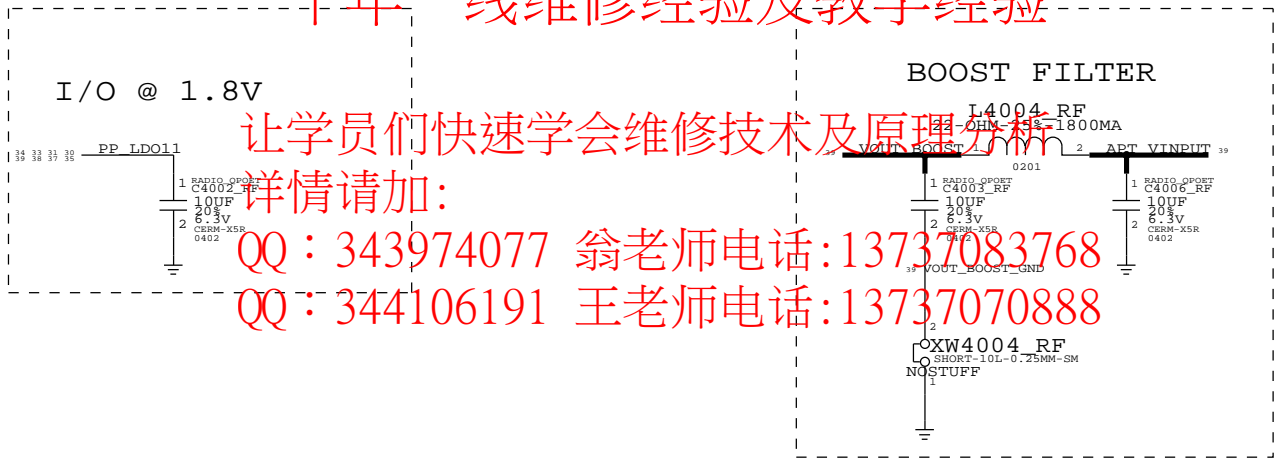
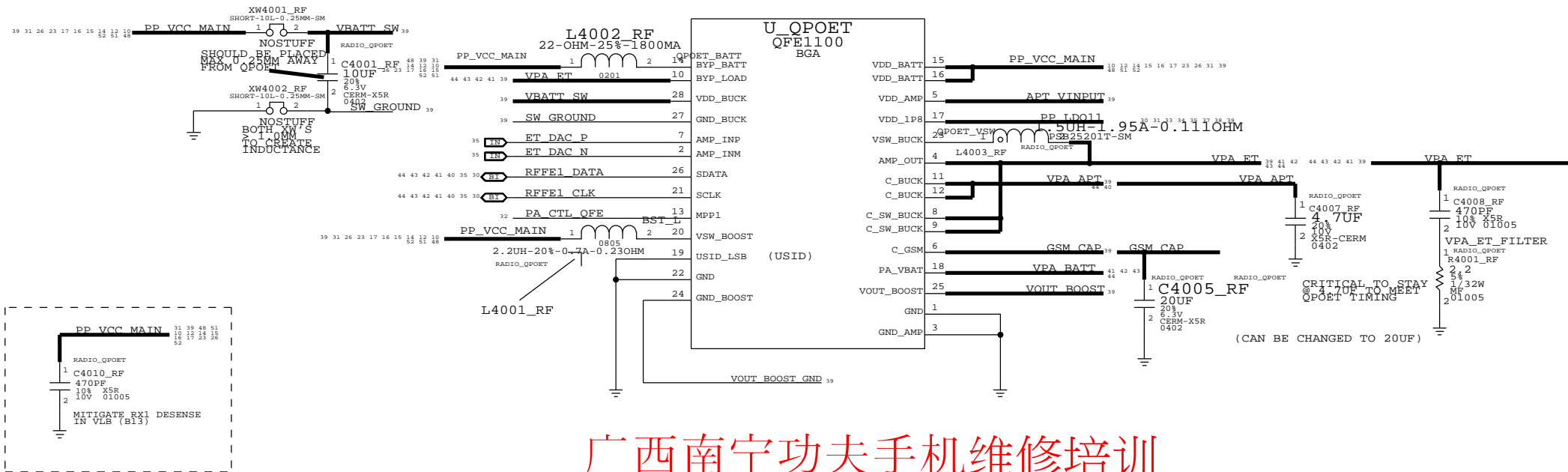
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C1110
R1102
L1104
U1101



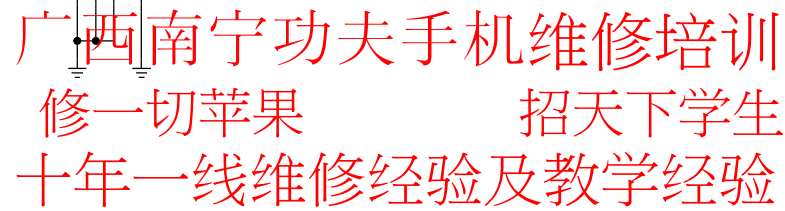
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C1208
R1200
L1204
U1201



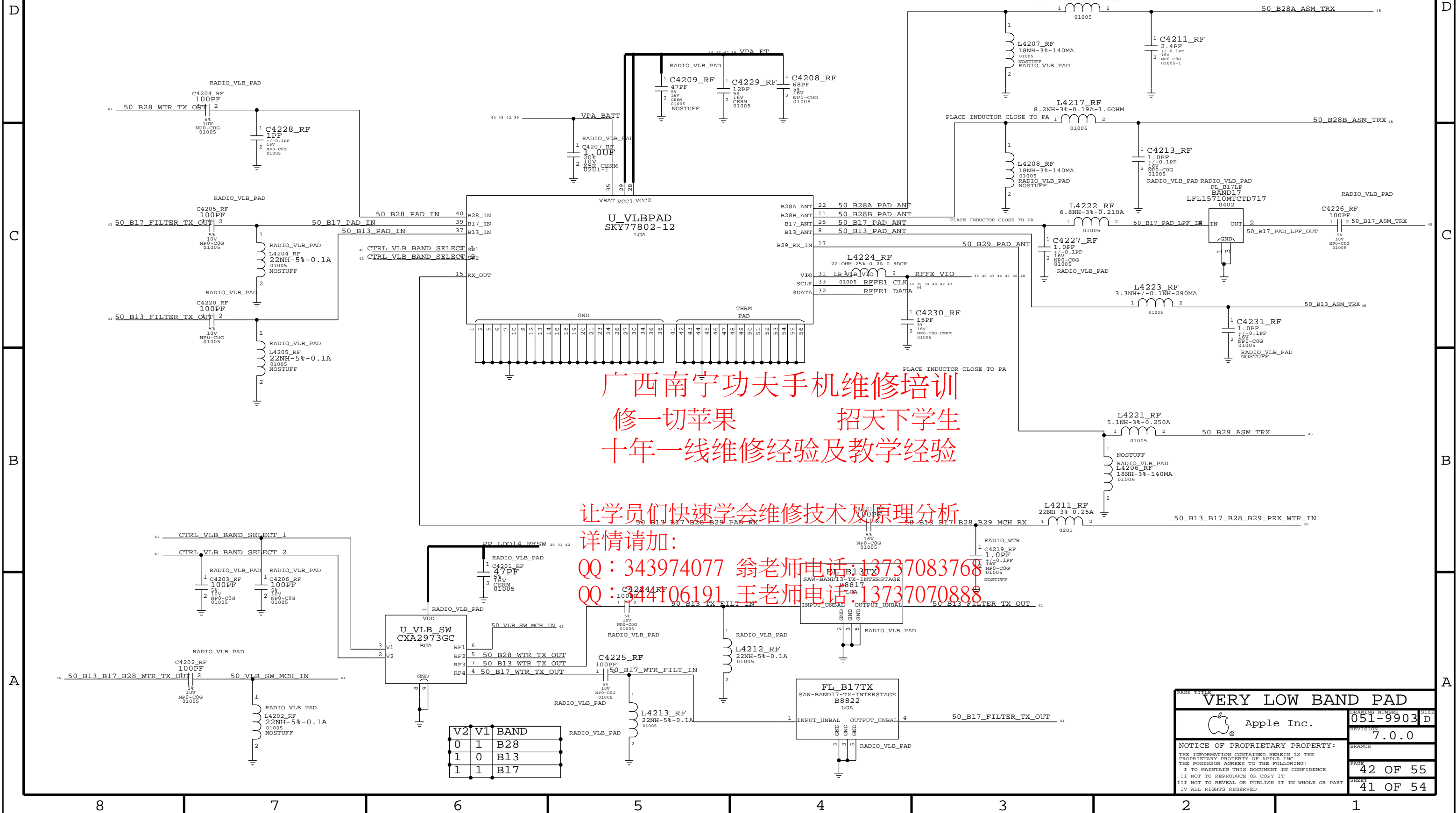
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C1332
R1300
L4215_RF
U1304

C1332
R1300
L4215_RF
U1304





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U1402



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MID BAND PAD (B1, B25, B3, B4, B34, B39)

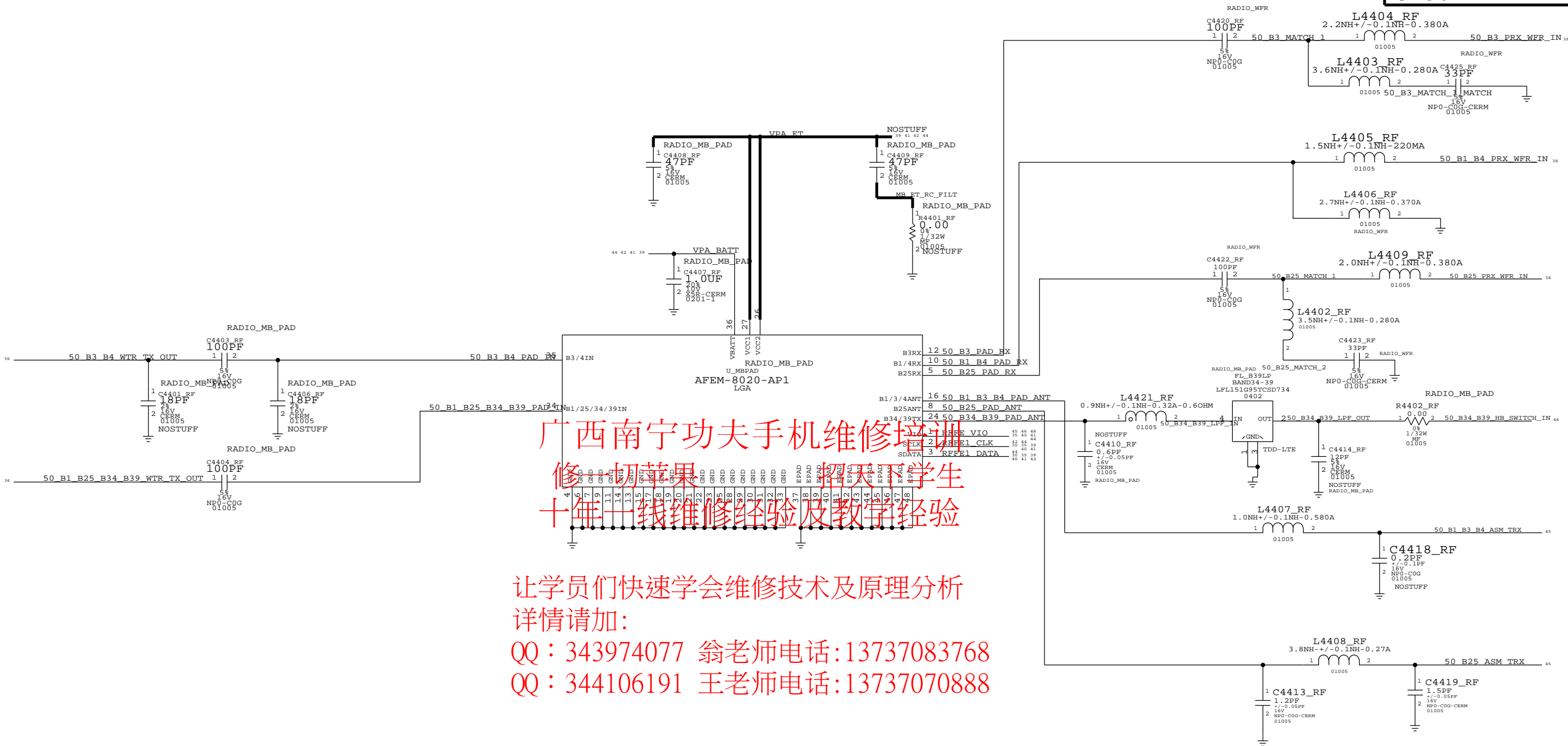
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C4426 RF

R1500

L4409 RF

U1501




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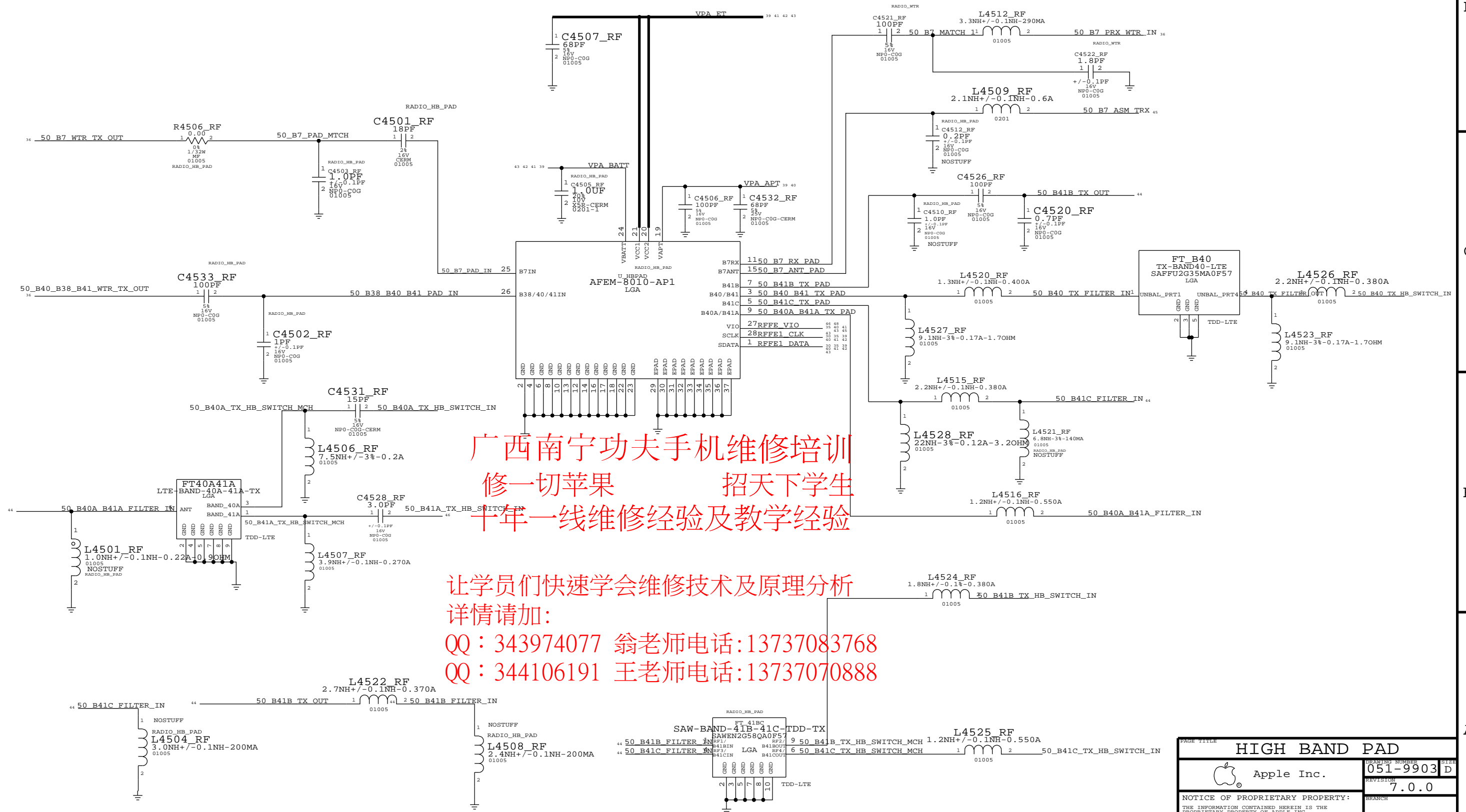
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HIGH BAND PAD (B7, B38, B40, B41, XGP)

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C4533_RF
R1600
L1616
U1601




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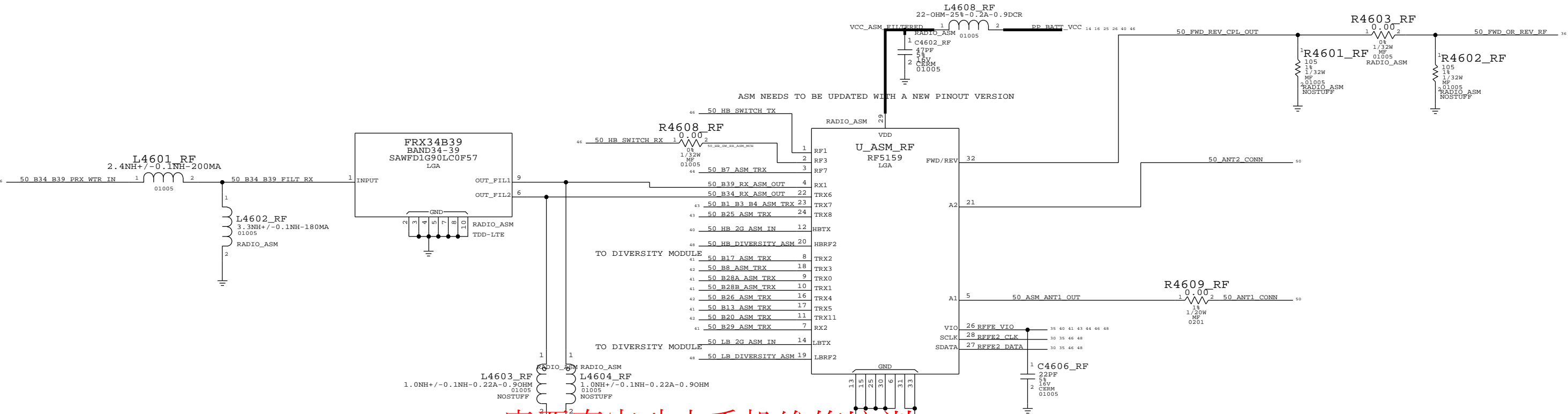
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C1702
R1700
L4608_RF
U1702



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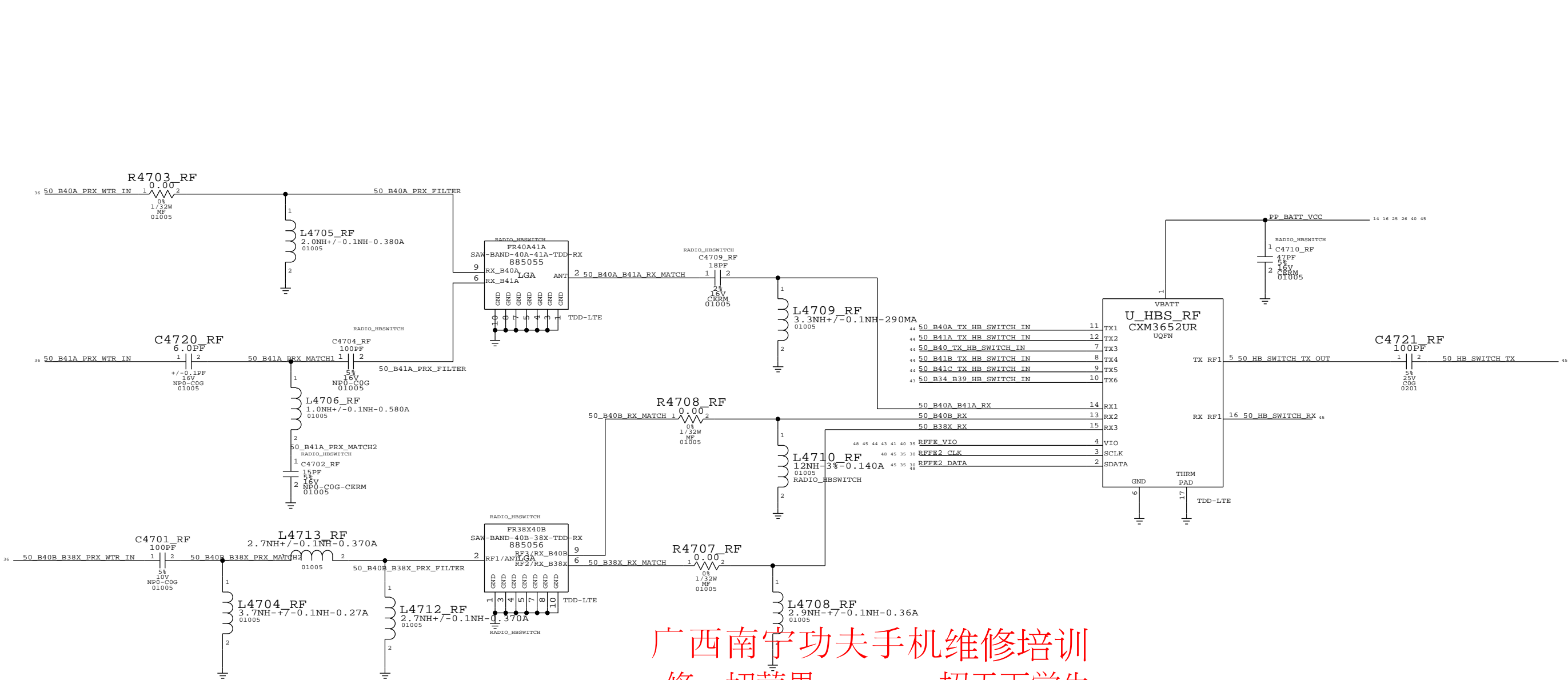
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# HIGH BAND SWITCH

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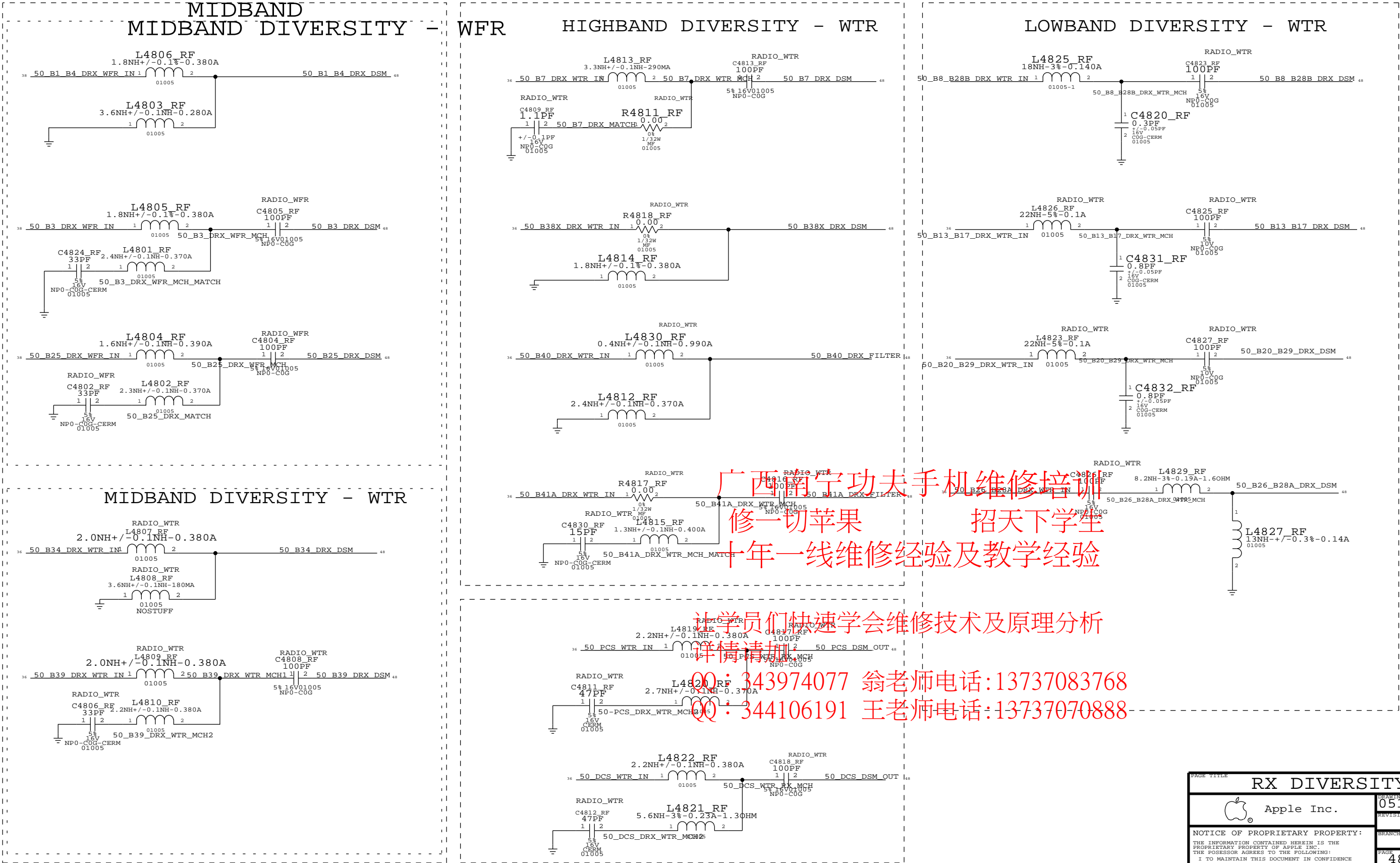
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RX DIVERSITY (1)

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C4826_RF
R1800
L1829
U1801



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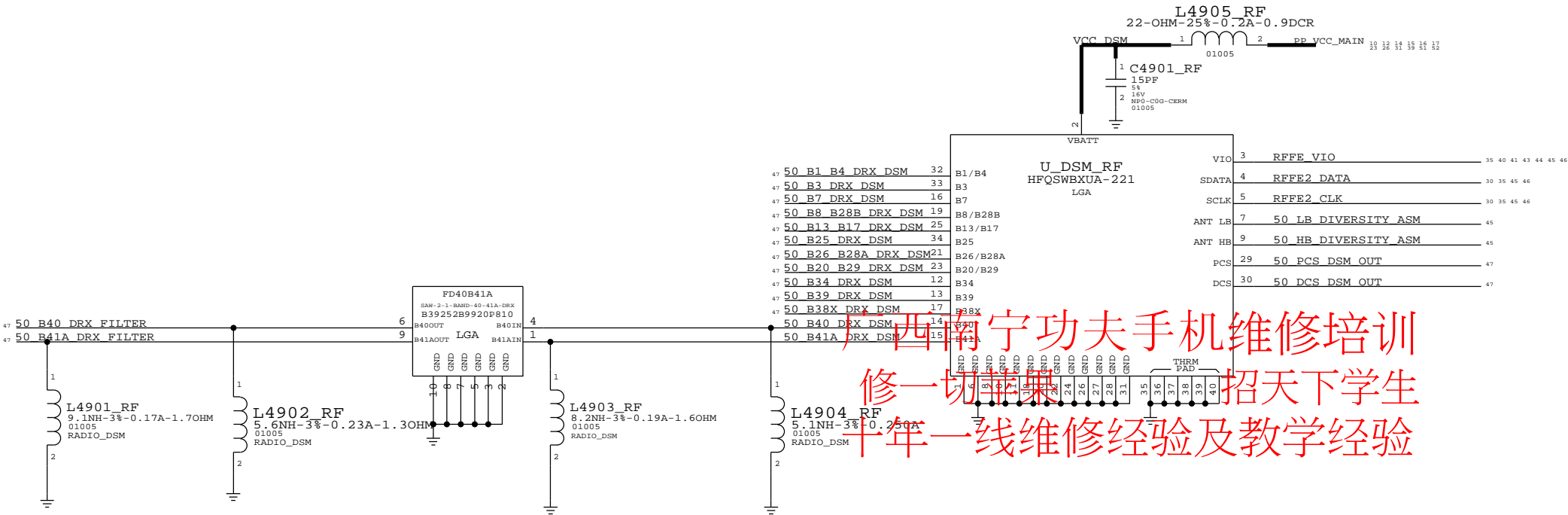
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# RX DIVERSITY (2)


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C1900
R1900
L1900
U1901



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# GPS

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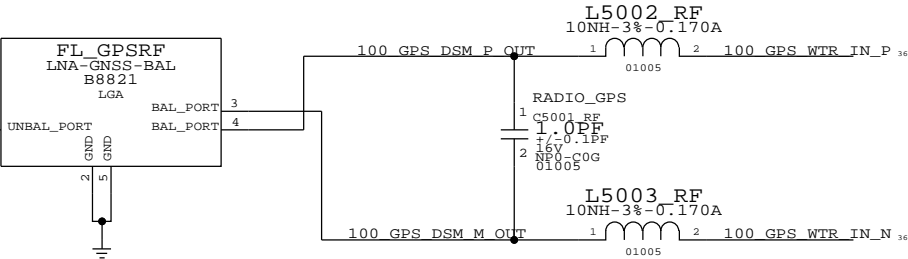
C1900
R1900
L1900
U1901


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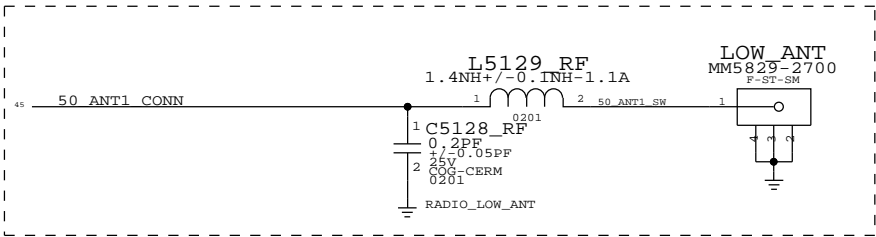
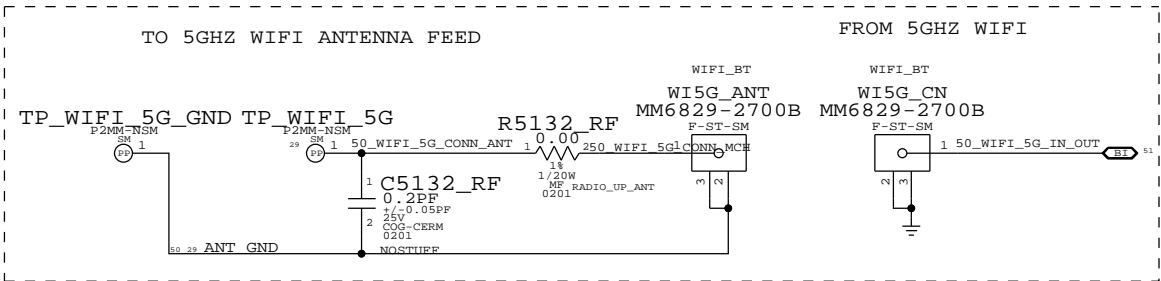
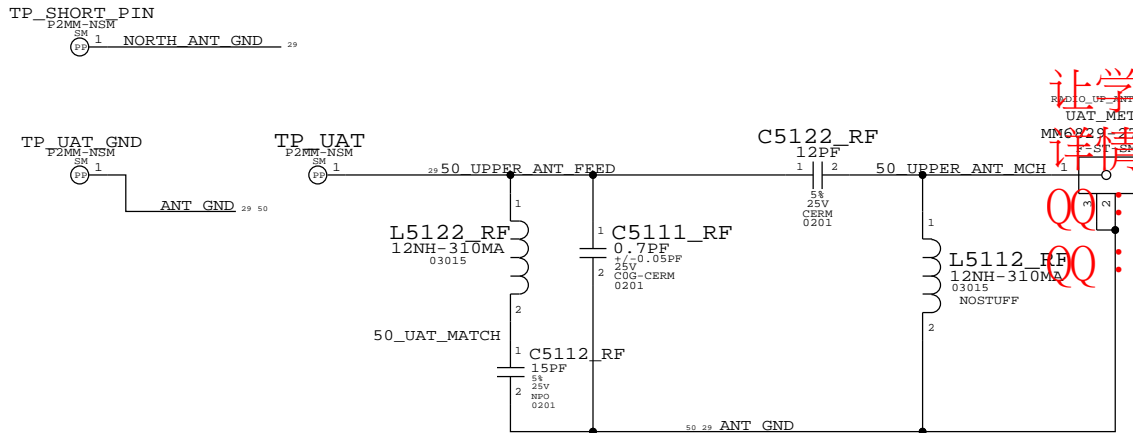
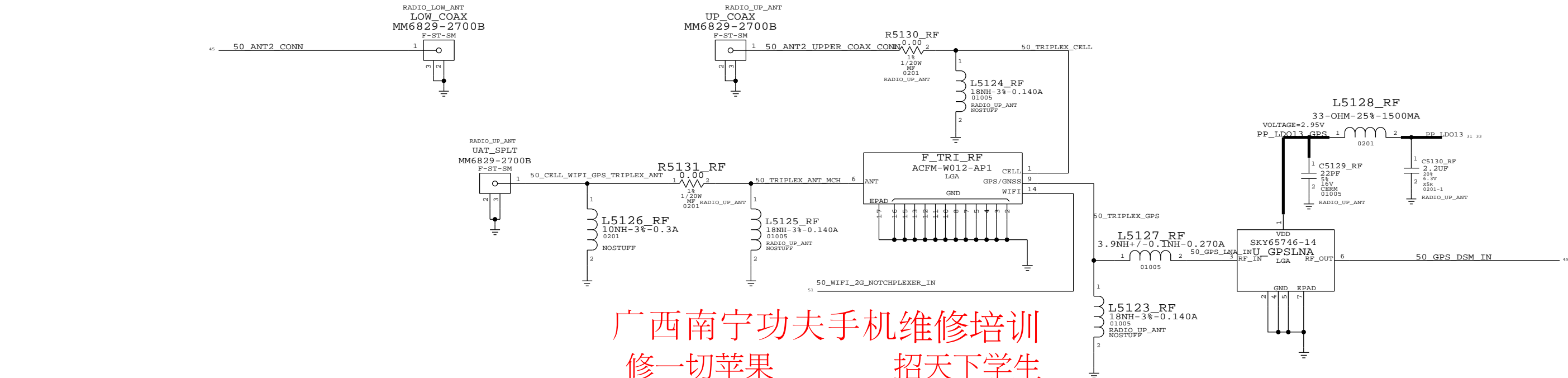



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# ANTENNA FEED'S

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TEST & COAX CONNECTOR FOR LOWER SECTION OF MLB



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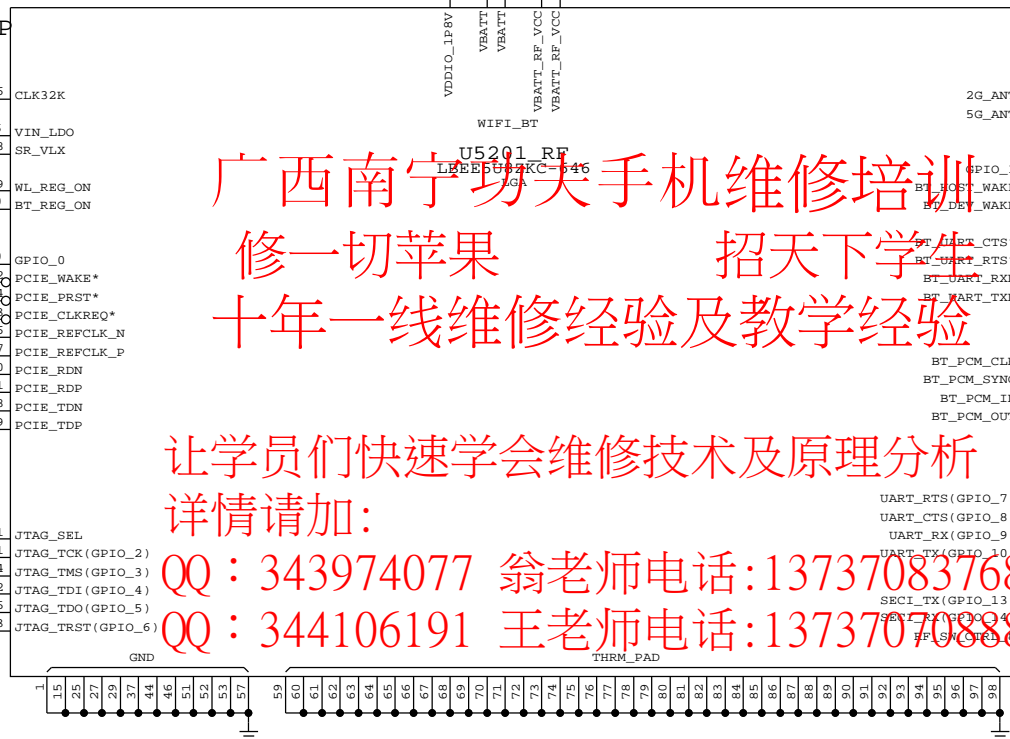
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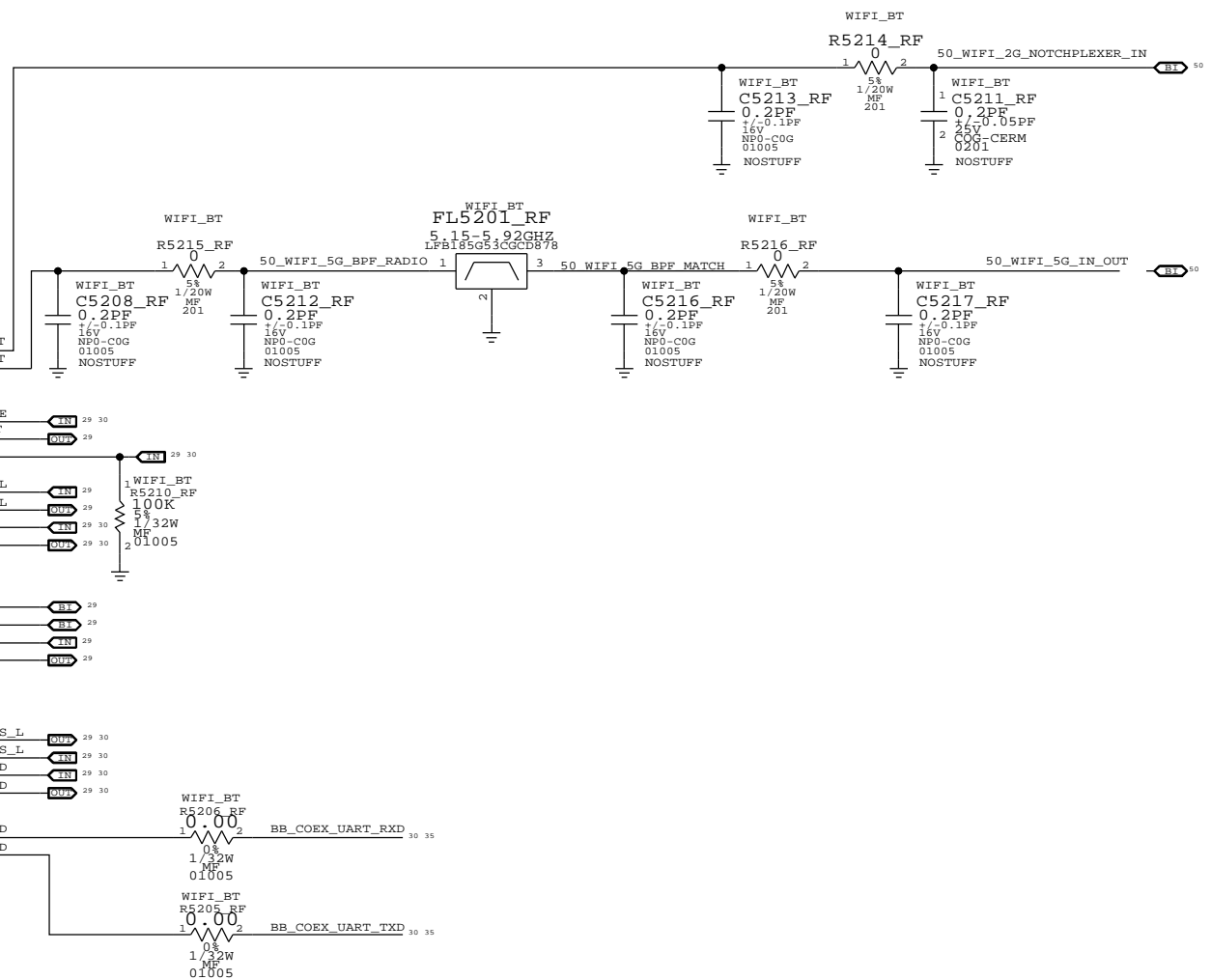
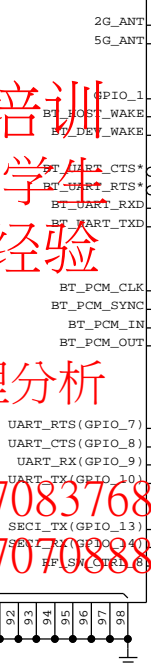



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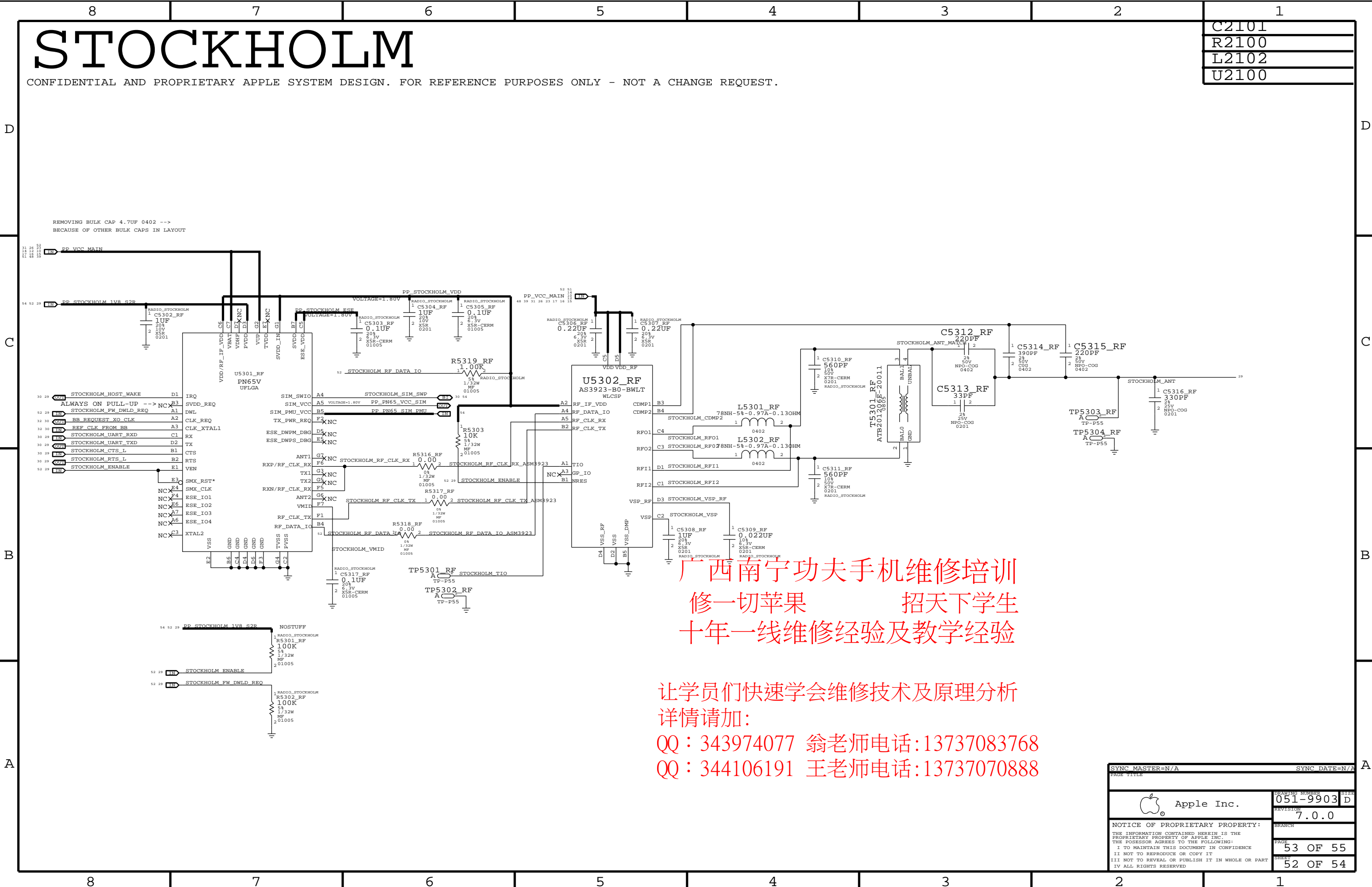
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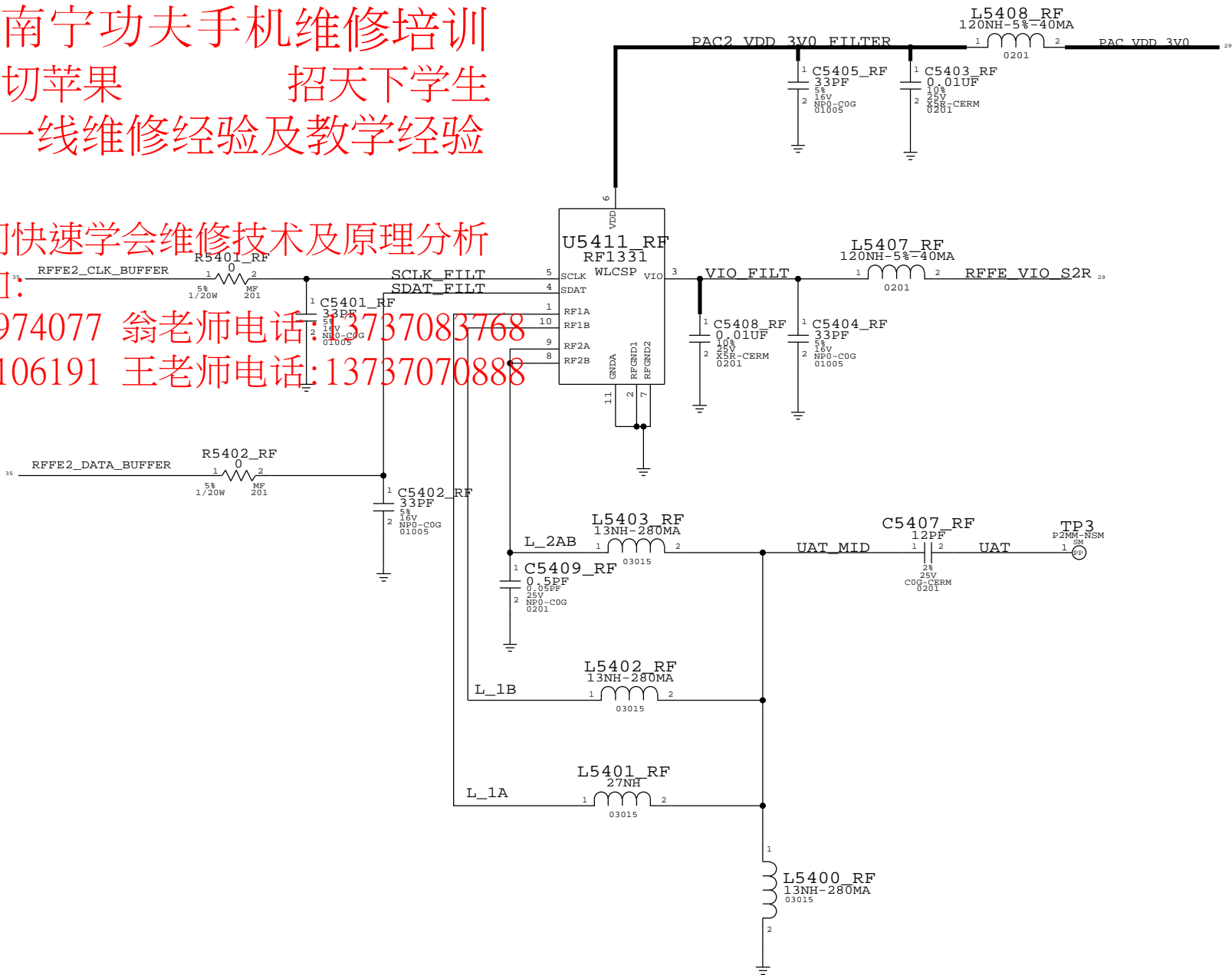



# ON-BOARD JUMPER FLEX

## UAT JUMPER

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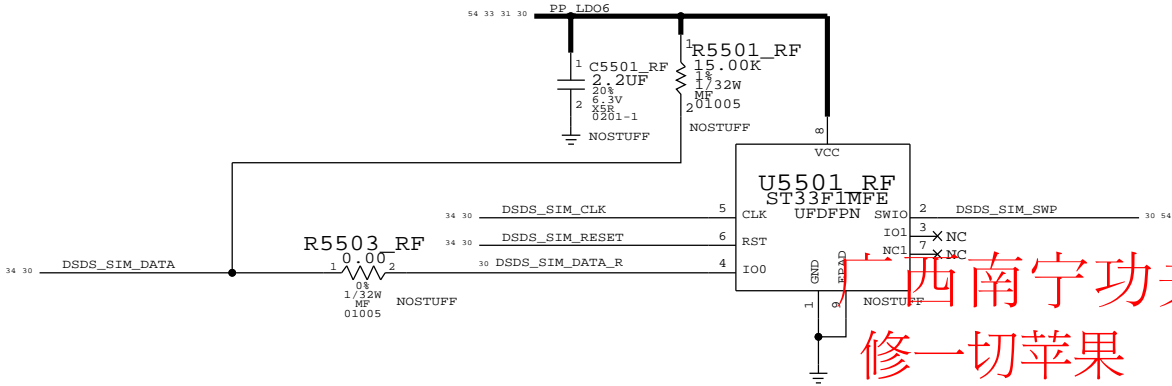
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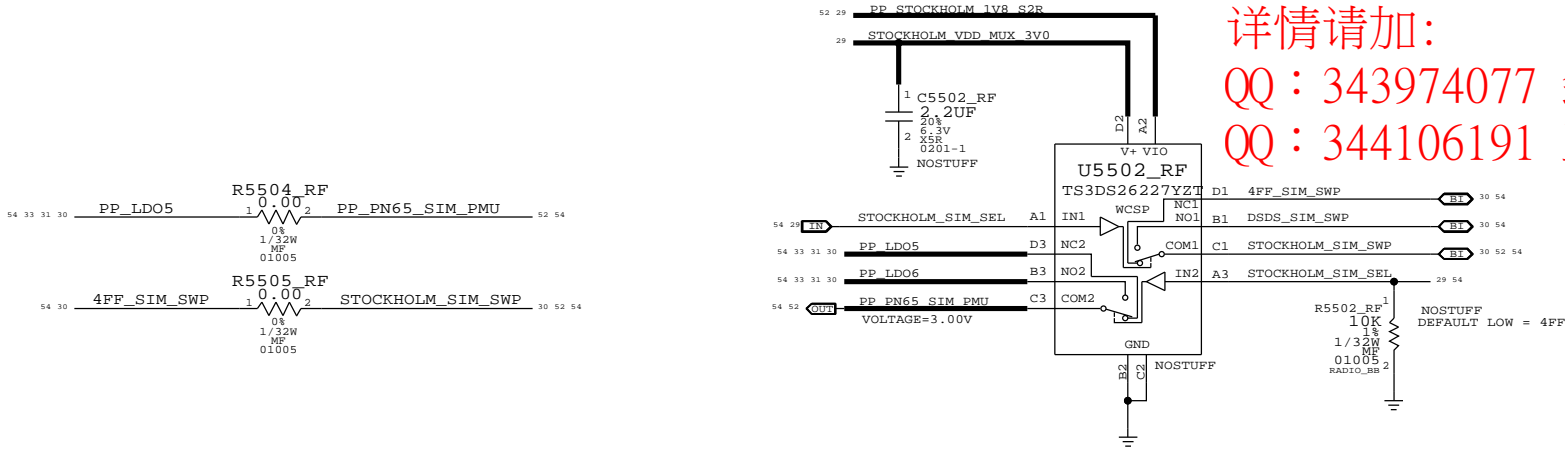



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